Service Manual

PMD330 /N1M, /U1B, /F1B PMD331 /N1M, /U1B, /F1B PMD340 /N1M, /U1B, /F1M

CD Player

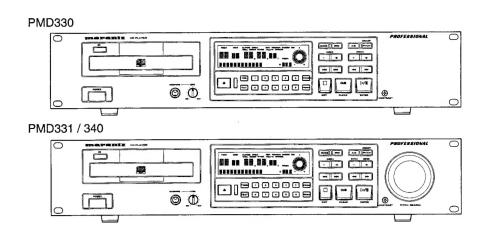




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Please use this service manual with referring to the user guide (D.F.U.) without fail. 修理の際は、必ず取扱説明書を準備し操作方法を確認の上作業を行ってください。



PMD330 / 331 / 340

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SHOCK, FIRE HAZARD SERVICE TEST:

CAUTION: After servicing this appliance and prior to returning to customer, measure the resistance between either primary AC cord connector pins (with unit NOT connected to AC mains and its Power switch ON), and the face or Front Panel of product and controls and chassis bottom.

Any resistance measurement less than 1 Megohms should cause unit to be repaired or corrected before AC power is applied, and verified before it is return to the user/customer.

Ref. UL Standard No. 813.

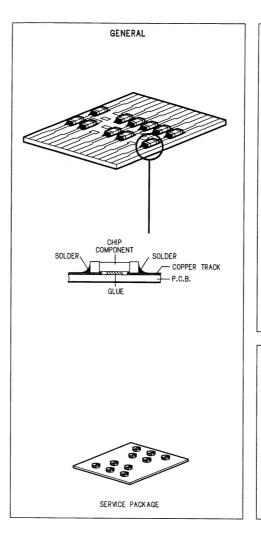
In case of difficulties, do not hesitate to contact the Technical Department at above mentioned address.

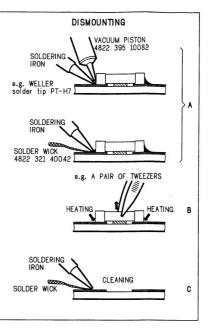
1. TECHNICAL SPECIFICATIONS

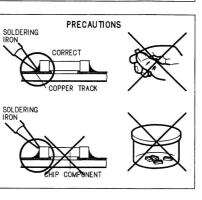
| | • | PMD330 | PMD331 / PMD340 | | | |
|------------------------|---------------------------|--------------------------------|--------------------------------|--|--|--|
| General specifications | | | • | | | |
| System | | Compact Disc - Digital Audio | | | | |
| Number of channe | ls | 2 | | | | |
| Compatible discs | | CD-DA, CD-R, CD- | RW (12 cm, 8 cm) | | | |
| Audio characteristics | | | | | | |
| Channels | | 2 chann | els | | | |
| Frequency charact | eristics | 20 Hz to 20 kH | z +/- 0.3 dB | | | |
| Dynamic range | | ≥ 90 dB (1 | kHz) | | | |
| S/N ratio | | ≥ 100 dB (1 | kHz) | | | |
| Total harmonic dist | ortion (THD) | 0.005 % | (1 kHz) | | | |
| Wow and flutter | | Quartz pr | recision | | | |
| Error correction me | ethod | Cross-interleave Read- | Solomon code (CIRC) | | | |
| Analog output | Pin jack,unbalanced (RCA) | 2.0V RMS | | | | |
| | XLR jack,balanced (XLR) | | +16 dBu /600 Ω, @ 0 dB FS | | | |
| | (variable range) | | (-11 dBu to +21 dBu, variable) | | | |
| Digital output | Pin jack (SPDIF) | 0.5 Vp-p/7 | | | | |
| | XLR jack (SPDIF) | | 3.5 Vp-p/110 Ω | | | |
| | optical connector | | -19 dBm | | | |
| Search precision | | 1 frame | | | | |
| Pitch control | | Maximum: +/-12 | 2% in 0.1% steps | | | |
| Pitch bend control | | +/- 8 % | | | | |
| Strat timing | | | 20 ms | | | |
| Remote control | | | | | | |
| Infrared remote cor | ntrol input | IN (IR s | ensor) | | | |
| RC5 remote contro | l input/output | RCA IN (INT/EXT switch)/OUT | | | | |
| Remote control inp | ut/output | | D-SUB 25-Pin female | | | |
| Optical anning method | | | | | | |
| Laser | | AlGaAs semio | onductor | | | |
| Wavelength | | 780 nr | n | | | |
| Signal system . | | | | | | |
| Sampling frequenc | у | 44.1 | (Hz | | | |
| Quantization | | 16-bit linear/o | hannel | | | |
| Power supply section | | | | | | |
| AC power supply | | /F: 100V, AC 50/60Hz, /N: 230V | , AC 50Hz, /U: 120V, AC 60Hz | | | |
| Power consumption | 1 | 12 W | 17 W | | | |
| Cabinet, etc. | | | | | | |
| External dimension | ns (W x H x D) | 483 x 100 x 325 mm (19 x 3- | 15/16 x 12- 13/16 inches) | | | |
| Weight | | 4.8 kg (10.6 lbs) | 4.9 kg (10.8 lbs) | | | |
| Operating tempera | ture range | + 5°C to + | | | | |
| Operating humidity | range | 5% to 90% (w | vithout dew) | | | |

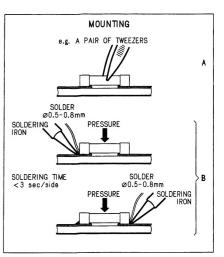
Due to our continuing efforts to improve our products, the specifications and appearance of this product are subject to change without prior notice.

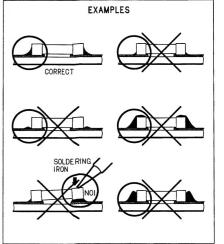
2. SERVICE HINTS











3. SERVICE TOOLS

| Audio signals disc | 4822 397 30184 |
|---|----------------|
| Disc without errors (SBC444)+ | |
| Disc with DO errors, black spots and fingerprints (SBC444A) | 4822 397 30245 |
| Disc (65 min 1kHz) without no pause | 4822 397 30155 |
| Max. diameter disc (58.0 mm) | 4822 397 60141 |
| Torx screwdrivers | |
| Set (straight) | 4822 395 50145 |
| Set (square) | 4822 395 50132 |
| 13th order filter | 4822 395 30204 |

4. ADJUSTMENT AND SERVICE MODE

1.1. Digital Output (Coaxial) Check

On the preset menu, set "D.OUT" to "ON".(PMD331/PMD340) Do waveform observation with the oscilloscope, and confirm the digital output level of JT01 to be 0.5Vp-p, square wave within $\pm 20\%$.

1.2. Balanced Output Adjustment (PMD331/PMD340)

1kHz, 0 dB are played back by using TEST disc.

Turn RB01 on the rear panel, and adjust the output of JB53 (Balanced Out L-CH).

Turn RB02 on the rear panel, and adjust the output of JB54 (Balanced Out R-CH).

Adjust each output level to 16 dBu, within ±0.5dB.

1.3. Service Mode

- With power off, simultaneously press the PLAY/PAUSE, MODE and TIME buttons, and at the same time, press the power button. At this time the LCD shows the model name and firmware version.
- 2) Next, press CUE button.
- At this time the LCD shows "Test: Version". (TEST MODE select menu)
- The NEXT and PREVIOUS buttons change the TEST MODE(refer to the chart below). The PLAY button selects it.
- Pressing the CUE button returns to the TEST MODE select menu.
- 6) Press the STOP button to exit the service mode.

| INDEX | TEST MODE | CONTENTS |
|---------|--------------|--|
| 1.3.1 | Version | MPU firmware version check |
| 1.3.2 | Display | LCD&LED test |
| 1.3.3 | Key&GPI | Confirmation of Buttons, GPI Control I/O and RC5 |
| 1.3.4 * | EE-PROM | Check of EEPROM Read/Write |
| 1.3.5 * | Pickup | Manual moving of the pickup |

* It is not usually necessary to confirm.

1.3.1. Model name and firmware version check

When the LCD shows "Test: Version", press the PLAY button, to see the model name and the MPU firmware version. Pressing the CUE button returns to the TEST MODE select menu.

1.3.2. LCD and LED test

- Set the LCD panel contrast adjustment screw to mechanical center. (you will feel a click.)
- 2) When the LCD shows "Test: Display", press the PLAY button.
- 3) The LCD and LED lights as the chart below.
- 4) Each time the PLAY/PAUSE button is pressed the LCD and LED change as shown in the chart below.
- Pressing the CUE button returns to the TEST MODE select menu.

4. 調整とサービスモード

1.1. Digital Output (Coax) 確認

Preset Menu で "D.OUT" を "ON" に設定する。(PMD331/PMD340) JT01のデジタル出力レベルをオシロスコープで波形観測をおこない 0.5Vp-p. +/-20%以内の矩形波である事を確認する。

1.2. Balanced Output 調整 (PMD331/PMD340)

TEST Disc を使用し 1kHz, 0dB を再生する。

背面パネルの RB01を回して JB53 (Balanced Out L-Ch)の出力を調整する。

背面パネルの RB02を回して JB54 (Balanced Out R-Ch)の出力を調整する。

各々の出力レベルを 16dBu, +/-0.5dB 以内に調整する。

1.3. SERVICE モードでの確認

電源 OFFの状態で、Play/Pause ボタン、Modeボタン、Timeボタンを同時に押しながら電源を入れる。

または電源ONの状態で赤外線リモコンからサービスコードを送ることにより、サービスモードに入る。この時、LCDにはモデル名とMPUファームウェアのバージョン表示される。次に、CUEボタンを押す。

この時、LCDの表示が "Test: Version " となる。(Test mode 選択画面)

NextとPrevious ボタンで Test mode(下表参照)を切り替え、Play ボタンで選択する。

CUEボタンで Test mode 選択画面の状態に戻る。 Stopボタンでサービスモードを終了する。

| 確認項目 | Test mode | 内容 |
|---------|-----------|---------------------------------|
| 1.3.1 | Version | MPUのファームウェアのパージョン表示 |
| 1.3.2 | Display | LCD&LED 表示点灯パト |
| 1.3.3 | Key&GPI | ポタン, GPI Control I/O, RC5 の入力表示 |
| 1.3.4 * | EE-PROM | EEPROM Read/Write のチェック |
| 1.3.5 * | Pickup | ピックアップを手動で動作させる |

* 印の項目は通常確認の必要は無い。

1.3.1. モデル名/プログラムバージョンの確認

"Test: Version" と表示されているときに、Play ボタン押すと、モデル名とMPUファームウェアのバージョンが表示される。 CUEボタンを押すと Test mode 選択画面 の状態に戻ります。

1.3.2. LCD 表示 & LED 点灯の確認

あらかじめ前面パネルのコントラスト調整用ボリュームRYO1をメカニカルセンターでクリックする位置に調整する。

"Test: Display" と表示されているときに、Play ボタン押すと、下記表に従ってLCDとLEDが点灯される。 Play ボタンを押す毎にLCDとLEDは下表の順に表示・点灯が切り替わる。

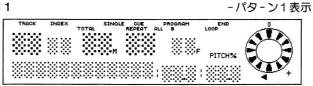
CUEボタンを押すと Test mode 選択画面 の状態に戻ります。

| | BUTTON GPI Control I/O | | | | | | | | | | |
|---|------------------------|-----|-------|------|-----|---------------|----------------|--------------|----------------|-------|--------------|
| | LCD | END | PITCH | PLAY | CUE | PLAY TALLY | PAUSE TALLY | CUE TALLY | FADER TALLY | INDEX | END TALLY |
| 1 | PATTERN 1 | 0 | × | 0 | × | 0 | × | 0 | . X | 0 | X |
| 2 | PATTERN 2 | × | 0 | × | 0 | × | 0 | × | 0 | × | 0 |
| 3 | All light up | 0 | Ο , | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| 4 | None light up | × | × | × | × | × | , X | × | × | X | × |

○: Light X: Not Light

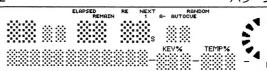
○→点灯、×→消灯

PATTERN 1



PATTERN 2

- パタ - ン2表示



1.3.3. Confirmation of Buttons, GPI Control I/O and RC5

- When the LCD shows "Test: Key&GPI", press the PLAY button.
- 2) The LCD shows "No Signal".
- Press a button, GPI Control I/O and RC5 are input, and the LCD changes as shown in the chart below.

1.3.3. ボタン、GPI Control I/O, RC5 の確認

"Test: Key&GPI" と表示されているときに、Play ボタン押すと "No Signal" と表示が変わり入力された信号源と種類を下記の表に従いLCDに表示する。

| FUNCTION | SW Input | GPI * Input | RC5 Input | FUNCTION | SW Input | GPI * Input | RC5 Input |
|--------------|-------------|----------------|--------------|---------------|-------------|----------------|--------------|
| Open/Close | 28 | - | 29 | Preset | 33 | | 34 |
| Time | 29 | | 30 | Index + | 17 | 8 | 18 |
| CD-Text | 30 | | 31 | Index - | 18 | 9 | 19 |
| Mode | 31 | | 32 | 0 - | 1 | | 2 |
| Stop *** | | | | 1 | 2 | | 3 |
| Cue **** | | | | 2 | 3 | | 4 |
| Play/Pause | 11 | | | 3 | 4 | | 5 |
| Play | | 1 | 12 | 4 | 5 | | 6 |
| Cue + Play | 13 | 4 | | 5 | 6 | | 7 |
| Pause | | 2 | 13 | 6 | 7 | | 8 |
| Next | 15 | 10 | 16 | 7 | 8 | | 9 |
| Previous | 16 | 11 | 17 | 8 | 9 | | 10 |
| FF | 19 | 6 | 20 | 9 | 10 | | 11 |
| REW | 20 | 7 | 21 | Pitch Bend +* | 26 | | 27 |
| END | 22 | | 23 | Pitch Bend -* | 27 | | 28 |
| A-B Repeat | 21 | | 22 | Service | | | 35 |
| Pitch + | 24 ** | 13 | 25 | Fader | | Fader | - |
| Pitch - | 25 ** | 14 | 26 | (Normal) | | Input | |
| Program | 32 | | 33 | Fader | | Fader | |
| Pitch On/Off | 23 | 15 | 24 | (Invert) | | Input | |

***: The service mode is exited.

****: The TEST MODE select menu is returned.

1.3.4. Check of EEPROM Read/Write

- When the LCD shows "Test: EE-PROM", press the PLAY button.
- Check of EEPROM Read/Write begins. The check takes about 1 minute. During the check pressing any button has no effect.
- 3) At this time the LCD shows as the following order.

 "ADDR (LOW)"--->"WRITE (LOW)"--->"WRITE (HIGH)"
 --->"PAGE WRITE"--->"EEPROM OK!"
- 4) If there is an error in the EEPROM, the LCD shows "EEPROM NG!".
- Pressing the CUE button returns to the TEST MODE select menu.

1.3.5. Manual moving of Pick up

menu.

- 1) When the LCD shows "Test: Pickup", press the PLAY button.
- 2) The LCD shows "Laser power". The laser diode turns on.
- 3) Press the NEXT button. The sled will move to the outside.
- 4) Press the PREVIOUS button. The sled will move to the inside.5) Pressing the CUE button returns to the TEST MODE select

- *: PMD331, PMD340 のみ。
- **: PMD330 のみ。

***: サービスモードが終了する。

**** Test mode 選択画面 の状態に戻ります。

1.3.4. EEPROM のRead/Writeチェック

"Test: EE-PROM"と表示されているときに、Play ボタン押すと EEPROMのRead/Write チェックを始めます。チェックに要する 時間は約1分間です。

チェック中は一切のボタン操作が無効となります。 この時 LCDには "ADDR (LOW)" --> "WRITE (LOW)" --> WRITE (HIGH) --> "PAGE WRITE" --> "EEPROM OK!"のように表示されます。

EEPROMに不具合がある場合は、"EEPROM NG!"が表示されます。 CUEボタンを押すと Test mode 選択画面 の状態に戻ります。

1.3.5. Pickup の手動移動

"Test: Pickup" と表示されているときに、Playボタン押すと "Laser power" と表示が変わり Laser Diode がONします。 Nextボタンで外周へ、Previous ボタンで内周へスレッドが移動します。

CUEボタンを押すと Test mode 選択画面 の状態に戻ります。

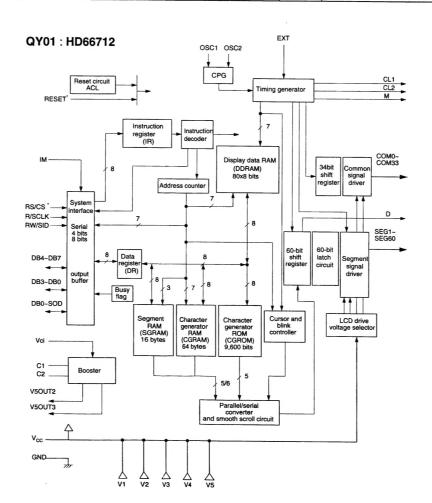
5. MICROPROCESSOR AND IC DATA

QU01: H8/3062

| PIN No. | PORT NAME | ICE I/O | 1/0 | ACTIVE | INITIAL | PULL UP/DOWN | SIGNAL NAME | CONNECT DEVICE NAME | CONNECT DEVICE PORT NAME | FUNCTION |
|------------|-------------------|------------|-----|--------|---------|-----------------|------------------------|---------------------------|--------------------------------|--|
| 1 | Vcc | sys | | | | | VCC | | | Connected to the system power supply (+5V |
| 2 | PB0 /TP8/TMO0 | 1/0 | 0 | | Low | | LCD_RS | HD66712 | RS | LCD driver register select. Instruction "L", Data register "H" |
| 3 | PB1 /TP9/TMIO1 | 1/0 | 0 | | High | | LCD_RW | HD66712 | RW | LCD driver READ/WRITE. READ "H", WRITE "L" |
| 4 | PB2 /TP10/TMO2 | 1/0 | 0 | | Low | - | LCD_E | HD66712 | E | LCD driver enable. Data READ/WRITE active signal. |
| 5 | /TP11/TMIO3 | 1/0 | 0 | Low | High | - | LCD_RESET | HD66712 | RESET | LCD driver reset. Normal "H", Reset "L" |
| 6 | /TP12 | 1/0 | 1/0 | - | Low | - | LCD_DB4 | HD66712 | DB4 | LCD driver data bit 0. |
| 7 | PB5 /TP13 | 1/0 | VO | - | Low | 1 | LCD_DB5 | HD66712 | DB5 | LCD driver data bit 1. |
| 8 | PB6 /TP14 | 1/0 | 1/0 | | Low | - | LCD_DB6 | HD66712 | DB6 | LCD driver data bit 2. |
| 9 | PB7 /TP15 | 1/0 | VO | - | Low | - | LCD_DB7 | HD66712 | DB7 | LCD driver data bit 3. |
| 10 | RESO //_FWE | sys | ı | Low | Low | EXT_DW | FEW | 74HC00 | | FLASH MPU program enable signal. Enabled "H" |
| 11 | Vss | sys | | | - | | VSS | | | Connected to the system power supply (0V). |
| 12 | P90/TxD0 | 0 | 0 | | Low | - | DEBUG_TXD | | | TXD for debug mode. |
| 13 | P91/TxD1 | 0 | 0 | - | Low | | FLASH_TXD | | | TXD for FLASH MPU program. |
| 14 | P92/RxD0 | 1 | 1 | - | Low | | DEBUG_RXD | | | RXD debug mode. |
| 15 | P93/RxD1 | 1 | ı | - | High | INT UP | CXD_SQSO /FLASH_RXD | CXD2585Q /74HC00 | sqso | Sub-Q 80bit/PCM peak level data input & CD-TEXT data input./RXD for FLASH MPU program. |
| 16 | P94 /SCK0/IRQ4 | 1/0 | ı | | Low | EXT_DW | SIF_SO | 74HC165 | | Parallel to serial IC (74HC165) data input. |
| 17 | P95 /SCK1/IRQ5 | 1/0 | 0 | | High | INT UP | CXD_SQCK | CXD2585Q | | Read out clock output for SQSO. |
| | P40 | 1/0 | 0 | - | High | | ESA_SDTI | RL5C357 | SDTI | Serial data output for ESA. |
| 19 | P41 | 1/0 | 0 | | High | OPEN | ESA_SCK | RL5C357 | SCK | Serial clock data output for ESA. |
| 20 | P42 | 1/0 | 0 | - | High | OPEN | ESA_XLT | RL5C357 | XLT | Serial latch data output for ESA. |
| 21 | P43 | 1/0 | 0 | Low | High | OPEN | ESA_XSOE | RL5C357 | | Enabled signal for ESA serial data. Enable "L" |
| 22 | Vss | sys | | - | | | VSS | | | Connected to the system power supply (0V). |
| 23 | P44 | 1/0 | 0 | Low | High | OPEN | ESA_XRST | RL5C357 | | System reset output for ESA. Reset "L" |
| 24 | P45 | 1/0 | 0 | Low | High | OPEN | ESA_XWRE | RL5C357 | | Write enable output for ESA. Enable "L" |
| 25 | P46 | 1/0 | 0 | Low | High | OPEN | ESA_XQOK | RL5C357 | | Sub-code Q signal output for ESA. OK "L" |
| 26 | P47 | 1/0 | 1 | - | High | EXT DW | ESA_SDTO | RL5C357 | SDTO | Serial data input from ESA. |
| _ | P30 | 1/0 | 1 | Low | High | | | RL5C357 | AMIN | Write enable signal from ESA. Disable "L" |
| 28 | P31 | 1/0 | 1 | High | Low | EXT_DW | ESA_CHDT | RL5C357 | | Data monitor input from ESA. Monitoring "H" |
| 29 | P32 | 1/0 | 0 | High | Low | - | CXD_RW_SEL | CXD2585Q | LOCK | RF gain select for CD-RW CD-RW "H", CD-DA & CD-R "L" |
| 30 | P33 | 1/0 | 0 | High | Low | | CXD_LDON | | | Laser diode ON/OFF control. |
| 31 | P34 | 1/0 | 1 | | Low | _ | CXD_FOK | CXD2585Q | FOK | Focus lock detect input. |

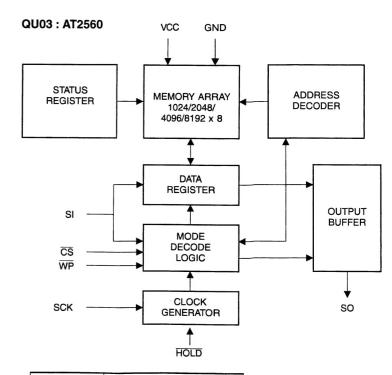
| 29 | P32 | 1/0 | 0 | High | Low | | CXD_RW_SEL | CXD2585Q | LOCK | RF gain select for CD-RW CD-RW "H", CD-DA & CD-R "L" |
|------------|-----------|------------|----------------|--------|---------|-----------------|--------------|---------------------------|----------------|--|
| 30 | P33 | 1/0 | 0 | High | Low | | CXD LDON | | | Laser diode ON/OFF control. |
| 31 | P34 | 1/0 | 1 | | Low | - | CXD_FOK | CXD2585Q | FOK | Focus lock detect input. |
| - | 1. 5. | | | | | | | | | |
| PIN No. | PORT NAME | DEV DEV | 1/0 | ACTIVE | INITIAL | PULL UP/DOWN | SIGNAL NAME | CONNECT DEVICE NAME | DEVICE PORT | |
| 32 | P35 | 1/0 | | Low | Low | - | CXD_LOCK | CXD2585Q | LOCK | GFS lock input. |
| 33 | P36 | 1/0 | 1 | | Low | | CXD_SSTP | CXD2585Q | SSTP | Disc inside detect input. |
| 34 | P37 | 1/0 | 0 | Low | High | - | CXD_XRST | CXD2585Q | XRST | System reset output. Reset "L" |
| 35 | Vcc | sys | - | | | - | vcc | | | Connected to the system power supply (+5V). |
| 36 | P10 | 1/0 | 0 | High | Low | - | CXD_DOUT_OFF | CXD2585Q | MD2 | Digital audio data output ON/OFF. ON "H" |
| 37 | P11 | 1/0 | 0 | High | High | - | CXD_MUTE | CXD2585Q | MUTE | Mute control output. Mute "H" |
| 38 | P12 | 1/0 | 0 | | High | | CXD_DATA | CXD2585Q | DATA | Serial data output for CXD2585Q. |
| 39 | P13 | 1/0 | 0 | | High | | CXD_XLAT | CXD2585Q | XLAT | Serial latch data output for CXD2585Q. |
| 40 | P14 | 1/0 | 0 | | High | | CXD_CLOK | CXD2585Q | CLOK | Serial clock data output. For CXD2585Q |
| 41 | P15 | 1/0 | 0 | | High | - | CXD_SCLK | CXD2585Q | SCLK | Clock output for SENS serial data read. |
| 42 | P16 | 1/0 | - 1 | | Low | - | CXD_SENS | CXD2585Q | SENS | SENS signal input. |
| 43 | P17 | 1/0 | I | | Low | EXT_DW | CXD_EMPH | CXD2585Q | ЕМРН | Emphasis enable/disable input. Enable "H", Disable "L" |
| 44 | Vss | sys | - | - | | - | vss | | | Connected to the system power supply (0V). |
| 45 | P20 | 1/0 | - 1 | Low | High | EXT_UP | SW_DATA0 | KEY INPUT | | Key matrix signal input. |
| 46 | P21 | 1/0 | ı | Low | High | EXT_UP | SW_DATA1 | KEY INPUT | | Ditto. |
| 47 | P22 | 1/0 | T | Low | High | EXT UP | SW DATA2 | KEY INPUT | | Ditto. |
| 48 | P23 | 1/0 | T | Low | High | EXT_UP | SW DATA3 | KEY INPUT | | Ditto. |
| 49 | P24 | 1/0 | -i- | Low | High | EXT UP | SW DATA4 | KEY INPUT | | Ditto. |
| 50 | P25 | 1/0 | | Low | High | EXT_UP | SW_DATA5 | KEY INPUT | | Ditto. |
| | P26 | 1/0 | ÷ | Low | High | EXT_UP | SW DATA6 | KEY INPUT | + | Ditto. |
| 51 | | 1/0 | + | | | EXT UP | SW_DATA7 | KEY INPUT | | |
| 52 | P27 | | | Low | High | | | | | Ditto. |
| 53 | P50 | 1/0 | 0 | | High | | SW_SCAN0 | KEY SCAN | | Key matrix signal output. |
| 54 | P51 | 1/0 | 0 | | High | - | SW_SCAN1 | KEY SCAN | | Ditto. |
| 55 | P52 | 1/0 | 0 | | High | | SW_SCAN2 | KEY SCAN | | Ditto. |
| 56 | P53 | 1/0 | 0 | - | High | | SW_SCAN3 | KEY SCAN | | Ditto. |
| 57 | Vss | sys | - | - | | - | vss | | | Connected to the system power supply (0V). |
| 58 | P60 | vo | 0 | - | Low | - | SIF_ST | 74HC4094 | STR | Serial strobe data output for serial to parallel IC (74HC4094). |
| 59 | P61 | 1/0 | 0 | | Low | OPEN | SIF_LD | 74HC165 | LS/ | Serial load data output for serial to paralle IC (74HC4094). |
| 60 | P62 | 1/0 | 0 | | Low | - | SIF_SI | 74HC4094 | DA | Serial data output for serial to parallel IC (74HC4094). |
| 61 | P67/Ф | - | - | - | | OPEN | PAI | | | System clock output. |
| 62 | STBY/ | sys | ı | High | High | EXT_UP | STBY | | | Standby mode input for MPU. Normal mode "H" |
| 63 | RES/ | sys | 1 | Low | High | EXT_UP | RES | | | System reset input for MPU. Reset "L" |
| 64 | NMI | sys | - 1 | | Low | EXT_DW | NMI | | | Not used. |
| 65 | Vss | sys | - | - | | - | vss | | | Connected to the system power supply (0V). |
| 66 | EXTAL | sys | ı | - | - | - | EXTAL | X'tal | | System clock input. Connected to 20MHz X'tal. |
| 67 | XTAL | sys | -1 | - | - | - | XTAL | X'tal | | System clock output. Connected to 20MHz X'tal. |
| 68 | Voc | sys | - | - | - | - | vcc | 7410 : | 014 | Connected to the system power supply (+5V). |
| 69 | P63 | VO. | 0 | - | Low | - | SIF_CK | 74HC4094 74HC165 | CK | Serial clock data output for ports expand IC |
| 70 | P64 | 1/0 | 0 | | Low | - | DAC_DATA | PCM1710 | MD/DM1 | Serial data output for D/A converter IC. |
| 71 | P65 | 1/0 | 0 | - | Low | - | DAC_CLK | PCM1710 | MC/DM2 | Serial clock data output for D/A converter IC. |
| 72 | P66 | 1/0 | 0 | - | Low | - | DAC_LAT | PCM1710 | ML/DSD | Serial latch data output for D/A converter |

| PIN No. | PORT NAME | DEV ICE I/O | 1/0 | ACTIVE | INITIAL | PULL UP/DOWN | SIGNAL NAME | CONNECT DEVICE NAME | CONNECT | FUNCTION |
|------------|--------------------|-------------------|-----|--------|---------|-----------------|----------------|---------------------------|---------|--|
| 73 | MD0 | sys | 1 | - | High | EXT_UP | MD0 | NAME | PORT | Mode select input for MPU. Mode7 "H" |
| 74 | MD1 | sys | 1 | - | High | EXT_UP | MD1 | | | Mode select input for MPU. Mode7 "H" |
| 75 | MD2 | sys | 1 | - | High | EXT_UP | MD2 | | | Mode select input for MPU. Mode7 "H", FLASH MPU program "L" |
| 76 | Avcc | sys | 1 | - | - | - | AVCC | | | Connected to the system power suppl (+5V). |
| 77 | Vref | sys | - 1 | | | | VREF | | | Ditto. |
| 78 | P70 /AN0 | | 1 | Low | High | EXT_UP | SW_SP0 | KEY INPUT | | PLAY/PAUSE button input. Active "L |
| 79 | P71/AN1 | 1 | 1 | Low | High | EXT_UP | SW_SP1 | KEY INPUT | | CUE button input. Active "L" |
| 80 | P72/AN2 | 1 | 1 | Low | High | EXT_UP | SW_FADER | KEY INPUT | | FADER SW input. Active "L" |
| 81 | P73/AN3 | 1 | 1 | - | High | EXT_UP | EEPROM_SO | AT25640 | SO | Serial data input for EEPROM. |
| 82 | P74/AN4 | 1 | ı | - | Low | EXT_UP | TRAY_SW_OPEN | TRAY OPEN SW | | Tray Open SW input. Open "L" |
| 83 | P75/AN5 | 1 | ı | - | Low | EXT_UP | TRAY_SW_CLOSE | TRAY CLOSE SW | | Tray Close SW input. Close "L" |
| 84 | P76 /AN6/DA0 | - | ı | - | Low | UP/DW | SYS_MODEL_SEL0 | RU09,RU05 | | (SEL0,SEL1); PMD330=(0,0), PMD331=(0,1) PMD340=(1,0) |
| 85 | P77 /AN7/DA1 | -1 | 1 | - | Low | UP/DW | SYS_MODEL_SEL1 | RU10,RU11 | | , , , |
| 86 | Avss | sys | ı | - | Low | - | AVSS | | | Connected to the system power supply (0V). |
| 87 | P80 /IRQ0/ | 1/0 | i | - | Low | - | CXD_SCOR | CXD2585Q | SCOR | Detected from Sub code think signal. Detected "H" |
| 88 | P81 /IRQ1/ | 1/0 | 0 | Low | High | EXT_UP | MONI_MUTE | | | Audio pre-mute control output. MUTE "L" |
| 89 | P82 /IRQ2/ | 1/0 | 0 | High | Low | EXT_UP | TRAY_DRV_OPEN | LB1641 | IN2 | (IN1,IN2), (1,0) CW LOAD, (0,1) CCW UNLOAD. |
| 90 | P83 /IRQ3/ | 1/0 | 0 | High | Low | EXT_UP | TRAY_DRV_CLOSE | LB1641 | IN1 | (0,0) or (1,1) STOP |
| 91 | P84 | 1/0 | 0 | Low | Low | - | AUDIO_MUTE | | | Audio mute control output. MUTE "L" |
| 92 | Vss | sys | | - | - | - | VSS | | | Connected to the system power supply (0V). |
| 93 | PA0 /TP0/TCLKA | 1/0 | , | Low | High | EXT_DW | ROT_DIAL_A | DIAL(+) | | Rotary encoder input. CW (Froward) "H", CCW (Reverse) "L" |
| 94 | PA1 /TP1/TCLKB | VO | 1 | Low | High | EXT_DW | ROT_DIAL_B | DIAL(-) | | 24puls/360° |
| 95 | PA2 /TP2/TIOCA0 | VO | 0 | Low | High | | RC5_MASK | | | IR signal mask SW. |
| 96 | PA3 /TP3/TIOCB0 | 1/0 | 0 | - 1 | Low | - | RC5_OUTPUT | | | RC5 signal output. |
| 9/ | PA4 /TP4/TIOCA1 | 1/0 | ı | - | Low | | RC5_INPUT | SPS-446-4 | | RC5 signal input. |
| 98 | PA5 /TP5/TIOCB1 | 1/0 | 0 | High | High | EXT_UP | EEPROM_CS | AT25640 | cs | Chip select output for EEPROM. Enable "H", Disable "L" |
| 99 | PA6 /TP6/TIOCA2 | 1/0 | 0 | - | High | EXT_UP | EEPROM_SI | AT25640 | SI | Serial data output for EEPROM. |
| | PA7 /TP7/TIOCB2 | 1/0 | 0 | - | High | EXT_UP | EEPROM_CLK | AT25640 | SCK | Serial clock data output for EEPROM. |



Q201/Q202:TDA7073A

| PIN | SYMBOL | DESCRIPTION |
|-----|--------|-------------------------|
| 1 | IN1- | negative input 1 |
| 2 | IN1+ | positive input 1 |
| 3 | n.c. | not connected |
| 4 | n.c. | not connected |
| 5 | VP | positive supply voltage |
| 6 | IN2+ | positive input 2 |
| 7 | IN2- | negative input 2 |
| 8 | n.c. | not connected |
| 9 | OUT2+ | positive output 2 |
| 10 | GND2 | ground 2 |
| 11 | n.c. | not connected |
| 12 | OUT2- | negative output 2 |
| 13 | OUT1- | negative output 1 |
| 14 | GND1 | ground 1 |
| 15 | n.c. | not connected |
| 16 | OUT1+ | positive output 1 |

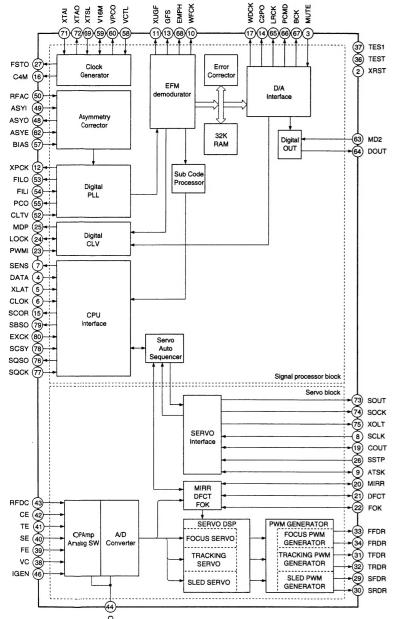


CS | 1 SO | 2 WP | 3

GND 4

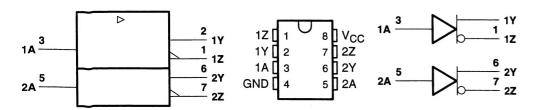
| Pin Name | Function |
|----------|-----------------------|
| CS | Chip Select |
| SCK | Serial Data Clock |
| SI | Serial Data Input |
| so | Serial Data Output |
| GND | Ground |
| VCC | Power Supply |
| WP | Write Protect |
| HOLD | Suspends Serial Input |
| NC | No Connect |
| DC | Don't Connect |

QD01: CXD2585Q



| Pin No. | Symbol | | I/O | Description |
|------------|--------------------|-----|---------|--|
| 1 | DV _{DD} 0 | - | | Power supply. |
| 2 | XRST | 1 | | System reset. Reset when low. |
| 3 | MUTE | 1 | | Mute input (low: off, high: on) |
| 4 | DATA | 1 | | Serial data input from CPU. |
| 5 | XLAT | 1 | | Latch input from CPU. Serial data is latched at the falling edge. |
| 6 | CLOK | 1 | | Serial data transfer clock input from CPU. |
| 7 | SENS | 0 | 1, 0 | SENS output to CPU. |
| 8 | SCLK | 1 | | SENS serial data readout clock input. |
| 9 | ATSK | 1/0 | 1, 0 | Anti-shock input/output. |
| 10 | WFCK | 0 | 1, 0 | WFCK output. |
| 11 | XUGF | 0 | 1, 0 | XUGF output. MNTO or RFCK is output by switching with the command. |
| 12 | XPCK | 0 | 1, 0 | XPCK output. MNTI is output by switching with the command. |
| 13 | GFS | 0 | 1, 0 | GFS output. MNT2 or XROF is output by switching with the command. |
| 14 | C2PO | 0 | 1, 0 | C2P0 output. MNT3 or GTOP is output by switching with the command. |
| 15 | SCOR | 0 | 1, 0 | Outputs a high signal when either subcode sync SO or S1 is detected. |
| 16 | C4M | 0 | 1, 0 | 4.2336MHz output. 1/4 frequency division output for V16M in CAV-W mode or variable pitch mode. |
| 17 | WDCK | 0 | 1, 0 | Word clock output. f = 2Fs. GRSCOR is output by the command switching. |
| 18 | DVss0 | - | - | Digital GND. |
| 19 | COUT | 1/0 | 1, 0 | Track count ,signal I/O. |
| 20 | MIRR | I/O | 1, 0 | Mirror signal I/O. |
| 21 | DFCT | 1/0 | 1, 0 | Detect signal I/O. |
| 22 | FOK | 1/0 | 1, 0 | Focus OK signal I/O. |
| 23 | PWMI | 1 | | Spindle motor external control input. |
| 24 | LOCK | 1/0 | 1, 0 | GFS is sampled at 460Hz; when GFS is high, this pin outputs a high signal. If GFS is low eight consecutive samples, this pin outputs low. Input when LKIN=1. |
| 25 | MDP | 0 | 1, Z, 0 | Spindle motor servo control output. |
| 26 | SSTP | 1 | | Disc innermost track detection signal input. |
| 27 | FSTO | 0 | 1, 0 | 2/3 frequency division output for XTAI pin. |
| 28 | DV _{DD} 1 | - | - | Digital power supply. |
| 29 | SFDR | 0 | 1, 0 | Sled drive output. |
| 30 | SRDR | 0 | 1, 0 | Sled drive output. |
| 31 | TFDR | 0 | 1, 0 | Tracking drive output. |
| 32 | TRDR | 0 | 1, 0 | Tracking drive output. |
| 33 | FFDR | 0 | 1, 0 | Focus drive output. |
| 34 | FRDR | 0 | 1, 0 | Focus drive output. |
| 35 | DVss1 | - | | Digital GND. |
| 36 | TEST | 1 | | Test. Normally, GND. |
| | • | | | <u> </u> |

QT52: SN75158



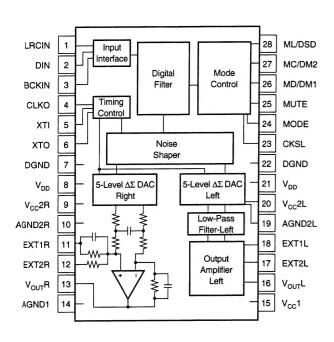
| 37 TES1 | Pin NO. | Symbol | I/O | | Description | | | | |
|---|------------|--------------------|-----|---------|--|--|--|--|--|
| 39 FE | 37 | TES1 | Ī | | Test. Normally, GND. | | | | |
| 40 SE I Sled error signal input. 41 TE I Tracking error signal input. 42 CE I Center servo analog input. 43 RFDC I RF signal input. 44 ADIO O Analog Test. No connected. 45 AVSSO - Analog GND. 46 IGEN I Constant current input for operational amplifier. 47 AVDDO Analog SPD. 48 ASYO O 1, 0 EFM full-swing output. (Iow = Vss, high = VDD) 49 ASYI I Asymmetry comparator voltage input. 50 RFAC I EFM signal input. 51 AVSSI - Analog GND. 52 CLTV I Multiplier VCO1 control voltage input. 53 FILO O Analog Master PLL filter output (slave = digital PLL). 54 FILI I Master PLL filter input. 55 PCO O 1, 2, 0 Master PLL charge pump output. 56 AVDD1 - Analog Dower supply. 57 BIAS I Asymmetry circuit constant current input. 58 VCTL I Wide-band EFM PLL VCO2 control voltage input. 59 V16M I/O 1, 0 Wide-band EFM PLL VCO2 control voltage input. 60 VPCO O 1, 2, 0 Wide-band EFM PLL Charge pump output. 61 DVDD2 - Digital Out on/oft control (low = off, high = on). 62 ASYE I Asymmetry circuit on/off (low = off, high = on). 63 MD2 I Digital Out on/oft control (low = off, high = on). 64 DOUT O 1, 0 D/A interface. Bit clock output. 1 FS 65 EMPH O 1, 0 D/A interface. Serial data output (two's complement, MSB first). 66 LRCK O 1, 0 D/A interface. Bit clock output. 67 BCK O 1, 0 D/A interface. Bit clock output. 68 EMPH O 1, 0 OUtputs a high signal when the playback disc has emphasis, and a low signal when there is no emphasis. 69 XTSL I Crystal selection input. Low when the crystal is 16.9344MHz; high when it is 3.8688MHZ. 70 DVSS2 - Digital GND. 71 XTAI I Crystal selection input. When the master clock is input externally, input it from this pin. | 38 | VC | 1 | | Center voltage input. | | | | |
| 41 TE | 39 | FE | T | | Focus error signal input. | | | | |
| 42 CE I Center servo analog input. 43 RFDC I RF signal input. 44 ADIO O Analog Test. No connected. 45 AV ss0 - Analog GND. 46 IGEN I Constant current input for operational amplifier. 47 AV DOO Analog Test. No connected. 48 ASYO O 1, 0 EFM full-swing output. (Iow = Vss., high = VDD) 48 ASYI I Asymmetry comparator voltage input. 50 RFAC I EFM signal input. 51 AV ss1 - Analog GND. 52 CLTV I Multiplier VCO1 control voltage input. 53 FILO O Analog Master PLL filter output (slave = digital PLL). 54 FILI I Master PLL filter input. 55 PCO O 1, Z, 0 Master PLL filter input. 56 AV bot - Analog power supply. 57 BIAS I Asymmetry circuit constant current input. 58 VCTL I Wide-band EFM PLL VCO2 control voltage input. 59 V16M I/O 1, 0 Wide-band EFM PLL VCO2 coscillation output. Serves as wide-band EFM PLL clock input by switching with the command. 60 VPCO O 1, Z, 0 Wide-band EFM PLL clock input by switching with the command. 61 DV bo2 - Digital power supply. 62 ASYE I Asymmetry circuit con/off (low = off, high = on). 63 MD2 I Digital Out on/oft control (low = off, high = on). 64 DOUT O 1, 0 Digital Out output. 65 LRCK O 1, 0 Digital Out output. 66 PCMD O 1, 0 Di/A interface. Era clock output. f = Fs 66 PCMD O 1, 0 Di/A interface. Serial data output (two's complement, MSB first). 67 BCK O 1, 0 Di/A interface. Serial data output (two's complement, MSB first). 68 EMPH O 1, 0 Outputs a high signal when the playback disc has emphasis, and a low signal when there is no emphasis. 69 XTSL I Crystal selection input. Low when the crystal is 16.9344MHz; high when it is 33.8688MHz. 70 DVss2 - Digital GND. 71 XTAI I Crystal selection input. When the master clock is input externally, input it from this pin. | 40 | SE | ı | | Sled error signal input. | | | | |
| 43 RFDC I RF signal input. 44 ADIO O Analog Test. No connected. 45 AVss0 Analog GND. 46 IGEN I Constant current input for operational amplifier. 47 AV bb O Analog power supply. 48 ASYO O 1, 0 EFM full-swing output. (low = Vss, high = VDD) 49 ASYI I Asymmetry comparator voltage input. 50 RFAC I EFM signal input. 51 AVss1 - Analog GND. 52 CLTV I Multiplier VCD1 control voltage input. 53 FiLO O Analog Master PLL filter output (slave = digital PLL). 54 FILI I Master PLL filter input. 55 PCO O 1, Z, 0 Master PLL charge pump output. 56 AVbb I - Analog power supply. 57 BIAS I Asymmetry circuit constant current input. 58 VCTL I Wide-band EFM PLL VCD2 control voltage input. 59 V16M I/O 1, 0 Wide-band EFM PLL VCD2 control voltage input. 60 VPCO O 1, Z, 0 Wide-band EFM PLL charge pump output. Serves as wide-band EFM PLL charge pump output. 61 DVbb 2 - Digital power supply. 62 ASYE I Asymmetry circuit constant current input. 63 MD2 I Digital Out onfort control (low = off, high = on). 64 DOUT O 1, 0 Digital Out output. 65 LRCK O 1, 0 D/A interface. LR clock output. f = Fs 66 PCMD O 1, 0 D/A interface. Brid data output (two's complement, MSB first). 67 BCK O 1, 0 D/A interface. Brid clock output. 68 EMPH O 1, 0 OUtputs a high signal when the playback disc has emphasis, and a low signal when there is no emphasis. 69 XTSL I Crystal oscillation circuit input. When the master clock is input externally, input it from this pin. | 41 | TE | ı | | Tracking error signal input. | | | | |
| 44 ADIO O Analog Test. No connected. 45 AV ss0 Analog GND. 46 IGEN I Constant current input for operational amplifier. 47 AV bb O Analog power supply. 48 ASYO O 1, 0 EFM full-swing output. (low = Vss, high = VDD) 49 ASYI I Asymmetry comparator voltage input. 50 RFAC I EFM signal input. 51 AV ss1 Analog GND. 52 CLTV I Multiplier VCO1 control voltage input. 53 FILO O Analog Master PLL filter output (slave = digital PLL). 54 FILI I Master PLL filter input. 55 PCO O 1, Z, 0 Master PLL charge pump output. 56 AV bb 1 Analog power supply. 57 BIAS I Asymmetry circuit constant current input. 58 VCTL I Wide-band EFM PLL VCO2 control voltage input. 59 V16M I/O 1, 0 Wide-band EFM PLL VCO2 costilation output. Serves as wide-band EFM PLL clook input by switching with the command. 60 VPCO O 1, Z, 0 Wide-band EFM PLL clook input by switching with the command. 60 VPCO O 1, Z, 0 Wide-band EFM PLL clook input by switching with the command. 61 DV bb 2 Digital power supply. 62 ASYE I Asymmetry circuit on/off (low = off, high = on). 63 MD2 I Digital Out on/oft control (low = off, high = on). 64 DOUT O 1, 0 D/A interface. LR clock output. f = Fs 66 PCMD O 1, 0 D/A interface. Bit clock output. f = Fs 67 BCK O 1, 0 D/A interface. Bit clock output. 68 EMPH O 1, 0 Outputs a high signal when the playback disc has emphasis, and a low signal when there is no emphasis. 69 XTSL I Crystal selection input. Low when the crystal is 16.9344MHz; high when it is 33.8688MHZ. 70 DVss2 Digital GND. 71 XTAI I Crystal selection input. When the master clock is input externally, input it from this pin. | 42 | CE | ı | | Center servo analog input. | | | | |
| 45 AVss0 Analog GND. 46 IGEN I Constant current input for operational amplifier. 47 AVbb0 Analog power supply. 48 ASYO O 1, 0 EFM full-swing output. (Iow = Vss, high = VDD) 49 ASYI I Asymmetry comparator voltage input. 50 RFAC I EFM signal input. 51 AVss1 Analog GND. 52 CLTV I Multiplier VCO1 control voltage input. 53 FILO O Analog Master PLL filter output (slave = digital PLL). 54 FILI I Master PLL filter input. 55 PCO O 1, Z, 0 Master PLL thrape pump output. 56 AVbb1 Analog power supply. 57 BIAS I Asymmetry circuit constant current input. 58 VCTL I Wide-band EFM PLL VC02 control voltage input. 59 V16M I/O 1, 0 Wide-band EFM PLL VC02 control voltage input. 59 V16M I/O 1, 0 Wide-band EFM PLL VC02 control voltage input. 60 VPCO O 1, Z, 0 Wide-band EFM PLL VC02 control voltage input. 61 DVbb2 - Digital power supply. 62 ASYE I Asymmetry circuit constant current input. Serves as wide-band EFM PLL cloak input by switching with the command. 63 MD2 I Digital Dout on/off (low = off, high = on). 64 DOUT O 1, 0 Digital Out on/oft control (low = off, high = on). 65 LRCK O 1, 0 D/A interface. Erail data output (two's complement, MSB first). 66 BCMD O 1, 0 D/A interface. Bit clock output. 67 BCK O 1, 0 D/A interface. Bit clock output. 68 EMPH O 1, 0 Outputs a high signal when the playback disc has emphasis, and a low signal when there is no emphasis. 69 XTSL I Crystal oscillation circuit input. When the master clock is input externally, input it from this pin. | 43 | RFDC | ī | | RF signal input. | | | | |
| 46 IGEN I Constant current input for operational amplifier. 47 AV b0 0 Analog power supply. 48 ASYO O 1, 0 EFM full-swing output. (low = Vss, high = VDD) 49 ASYI I Asymmetry comparator voltage input. 50 RFAC I EFM signal input. 51 AV ss1 Analog GND. 52 CLTV I Multiplier VCO1 control voltage input. 53 FILO O Analog Master PLL filter output (slave = digital PLL). 54 FILI I Master PLL filter input. 55 PCO O 1, Z, 0 Master PLL charge pump output. 56 AV b1 Analog power supply. 57 BIAS I Asymmetry circuit constant current input. 58 VCTL I Wide-band EFM PLL VC02 control voltage input. 59 V16M I/O 1, 0 Wide-band EFM PLL VC02 control voltage input. 60 VPCO O 1, Z, 0 Wide-band EFM PLL VC02 control voltage input. 61 DV b02 Digital power supply. 62 ASYE I Asymmetry circuit constant current input. Serves as wide-band EFM PLL clock input by switching with the command. 63 MD2 I Digital power supply. 64 DOUT O 1, 0 Digital Out on/oft control (low = oft, high = on). 65 LRCK O 1, 0 D/A interface. LR clock output. f = Fs 66 PCMD O 1, 0 D/A interface. Serial data output (two's complement, MSB first). 67 BCK O 1, 0 D/A interface. Bit clock output. 68 EMPH O 1, 0 Outputs a high signal when the playback disc has emphasis, and a low signal when there is no emphasis. 69 XTSL I Crystal selection input. Low when the crystal is 16.9344MHz; high when it is 33.8688MHZ. 70 DVss2 Digital GND. 71 XTAI I Crystal oscillation circuit input. When the master clock is input externally, input it from this pin. | 44 | ADIO | 0 | Analog | Test. No connected. | | | | |
| 47 AV DD Analog power supply. 48 ASYO O 1, 0 EFM full-swing output. (low = Vss, high = VDD) 49 ASYI I Asymmetry comparator voltage input. 50 RFAC I EFM signal input. 51 AV ss1 - Analog GND. 52 CLTV I Multiplier VCO1 control voltage input. 53 FILO O Analog Master PLL filter output (slave = digital PLL). 54 FILI I Master PLL filter input. 55 PCO O 1, Z, 0 Master PLL charge pump output. 56 AV DD 1 - Analog power supply. 57 BIAS I Asymmetry circuit constant current input. 58 VCTL I Wide-band EFM PLL VCO2 control voltage input. 59 V16M I/O 1, 0 Wide-band EFM PLL VCO2 oscillation output. Serves as wide-band EFM PLL charge pump output. 60 VPCO O 1, Z, 0 Wide-band EFM PLL charge pump output. 61 DV DD 2 - Digital power supply. 62 ASYE I Asymmetry circuit on/off (low = off, high = on). 63 MD2 I Digital Out on/oft control (low = off, high = on). 64 DOUT O 1, 0 D/A interface. LR clock output. f = Fs 66 PCMD O 1, 0 D/A interface. Serial data output (two's complement, MSB first). 67 BCK O 1, 0 D/A interface. Bit clock output. 68 EMPH O 1, 0 Outputs a high signal when the playback disc has emphasis, and a low signal when there is no emphasis. 69 XTSL I Crystal selection input. Low when the crystal is 16.9344MHz; high when it is 33.8688MHZ. 70 DVss2 - Digital GND. 71 XTAI I Crystal oscillation circuit input. When the master clock is input externally, input it from this pin. | 45 | AVss0 | - | - | Analog GND. | | | | |
| 48 ASYO O 1, 0 EFM full-swing output. (low = Vss, high = VDD) 49 ASYI I Asymmetry comparator voltage input. 50 RFAC I EFM signal input. 51 AVss1 Analog GND. 52 CLTV I Multiplier VCO1 control voltage input. 53 FILO O Analog Master PLL filter output (slave = digital PLL). 54 FILI I Master PLL filter input. 55 PCO O 1, Z, 0 Master PLL charge pump output. 56 AVbb1 Analog power supply. 57 BIAS I Asymmetry circuit constant current input. 58 VCTL I Wide-band EFM PLL VC02 control voltage input. 59 V16M I/O 1, 0 Wide-band EFM PLL VC02 oscillation output. Serves as wide-band EFM PLL charge pump output. 60 VPCO O 1, Z, 0 Wide-band EFM PLL charge pump output. 61 DVob2 - Digital power supply. 62 ASYE I Asymmetry circuit on/off (low = off, high = on). 63 MD2 I Digital Out on/oft control (low = off, high = on). 64 DOUT O 1, 0 D/A interface. LR clock output. f = Fs 66 PCMD O 1, 0 D/A interface. Serial data output (two's complement, MSB first). 67 BCK O 1, 0 D/A interface. Bit clock output. 68 EMPH O 1, 0 Outputs a high signal when the playback disc has emphasis, and a low signal when there is no emphasis. 69 XTSL I Crystal selection input. Low when the crystal is 16.9344MHz; high when it is 33.8688MHZ. 70 DVss2 - Digital GND. 71 XTAI I Crystal oscillation circuit input. When the master clock is input externally, input it from this pin. | 46 | IGEN | ī | | Constant current input for operational amplifier. | | | | |
| 49 ASYI I Asymmetry comparator voltage input. 50 RFAC I EFM signal input. 51 AVss1 - Analog GND. 52 CLTV I Multiplier VCO1 control voltage input. 53 FILO O Analog Master PLL filter output (slave = digital PLL). 54 FILI I Master PLL filter input. 55 PCO O 1, Z, 0 Master PLL charge pump output. 56 AVoo1 - Analog power supply. 57 BIAS I Asymmetry circuit constant current input. 58 VCTL I Wide-band EFM PLL VC02 control voltage input. 59 V16M I/O 1, 0 Wide-band EFM PLL VC02 oscillation output. Serves as wide-band EFM PLL clock input by switching with the command. 60 VPCO O 1, Z, 0 Wide-band EFM PLL charge pump output. 61 DVoo2 - Digital power supply. 62 ASYE I Asymmetry circuit on/off (low = oft, high = on). 63 MD2 I Digital Out on/oft control (low = off, high = on). 64 DOUT O 1, 0 Digital Out output. 65 LRCK O 1, 0 D/A interface. LR clock output. f = Fs 66 PCMD O 1, 0 D/A interface. Serial data output (two's complement, MSB first). 67 BCK O 1, 0 D/A interface. Serial data output (two's complement, MSB first). 68 EMPH O 1, 0 Outputs a high signal when the playback disc has emphasis, and a low signal when there is no emphasis. 69 XTSL I Crystal selection input. Low when the crystal is 16.9344MHz; high when it is 33.8688MHZ. 70 DVss2 - Digital GND. 71 XTAI I Crystal oscillation circuit input. When the master clock is input externally, input it from this pin. | 47 | AV DD 0 | | | Analog power supply. | | | | |
| 50 RFAC I EFM signal input. 51 AVss1 Analog GND. 52 CLTV I Multiplier VCO1 control voltage input. 53 FILO O Analog Master PLL filter output (slave = digital PLL). 54 FILI I Master PLL filter input. 55 PCO O 1, Z, 0 Master PLL charge pump output. 56 AVoo1 Analog power supply. 57 BIAS I Asymmetry circuit constant current input. 58 VCTL I Wide-band EFM PLL VC02 control voltage input. 59 V16M I/O 1, 0 Wide-band EFM PLL VC02 oscillation output. Serves as wide-band EFM PLL clock input by switching with the command. 60 VPCO O 1, Z, 0 Wide-band EFM PLL charge pump output. 61 DVoo2 - Digital power supply. 62 ASYE I Asymmetry circuit on/off (low = oft, high = on). 63 MD2 I Digital Out on/oft control (low = off, high = on). 64 DOUT O 1, 0 Digital Out output. 65 LRCK O 1, 0 D/A interface. LR clock output. f = Fs 66 PCMD O 1, 0 D/A interface. Serial data output (two's complement, MSB first). 67 BCK O 1, 0 D/A interface. Serial data output (two's complement, MSB first). 68 EMPH O 1, 0 Outputs a high signal when the playback disc has emphasis, and a low signal when there is no emphasis. 69 XTSL I Crystal selection input. Low when the crystal is 16.9344MHz; high when it is 33.8688MHz. 70 DVss2 - Digital GND. 71 XTAI I Crystal oscillation circuit input. When the master clock is input externally, input it from this pin. | 48 | ASYO | 0 | 1, 0 | EFM full-swing output. (low = Vss, high = VDD) | | | | |
| 51 AVss1 Analog GND. 52 CLTV I Multiplier VCO1 control voltage input. 53 FILO O Analog Master PLL filter output (slave = digital PLL). 54 FILI I Master PLL filter input. 55 PCO O 1, Z, 0 Master PLL charge pump output. 56 AVbb1 Analog power supply. 57 BIAS I Mide-band EFM PLL VC02 control voltage input. 58 VCTL I Wide-band EFM PLL VC02 control voltage input. 59 V16M I/O 1, 0 Wide-band EFM PLL VC02 oscillation output. Serves as wide-band EFM PLL clock input by switching with the command. 60 VPCO O 1, Z, 0 Wide-band EFM PLL charge pump output. 61 DVbb2 Digital power supply. 62 ASYE I Asymmetry circuit on/off (low = oft, high = on). 63 MD2 I Digital Out on/oft control (low = off, high = on). 64 DOUT O 1, 0 Digital Out output. 65 LRCK O 1, 0 D/A interface. LR clock output. f = Fs 66 PCMD O 1, 0 D/A interface. Serial data output (two's complement, MSB first). 67 BCK O 1, 0 D/A interface. Bit clock output. 68 EMPH O 1, 0 Outputs a high signal when the playback disc has emphasis, and a low signal when there is no emphasis. 69 XTSL I Crystal selection input. Low when the crystal is 16.9344MHz; high when it is 33.8688MHZ. 70 DVss2 - Digital GND. 71 XTAI I Crystal oscillation circuit input. When the master clock is input externally, input it from this pin. | 49 | ASYI | ī | | Asymmetry comparator voltage input. | | | | |
| 52 CLTV I Multiplier VCO1 control voltage input. 53 FILO O Analog Master PLL filter output (slave = digital PLL). 54 FILI I Master PLL filter input. 55 PCO O 1, Z, 0 Master PLL charge pump output. 56 AV bo 1 Analog power supply. 57 BIAS I Asymmetry circuit constant current input. 58 VCTL I Wide-band EFM PLL VC02 control voltage input. 59 V16M I/O 1, 0 Wide-band EFM PLL VC02 oscillation output. Serves as wide-band EFM PLL clock input by switching with the command. 60 VPCO O 1, Z, 0 Wide-band EFM PLL charge pump output. 61 DV bo 2 Digital power supply. 62 ASYE I Asymmetry circuit on/off (low = off, high = on). 63 MD2 I Digital Out on/oft control (low = off, high = on). 64 DOUT O 1, 0 Digital Out output. 65 LRCK O 1, 0 D/A interface. LR clock output. f = Fs 66 PCMD O 1, 0 D/A interface. Serial data output (two's complement, MSB first). 67 BCK O 1, 0 D/A interface. Bit clock output. 68 EMPH O 1, 0 Outputs a high signal when the playback disc has emphasis, and a low signal when there is no emphasis. 69 XTSL I Crystal selection input. Low when the crystal is 16.9344MHz; high when it is 33.8688MHZ. 70 DVss2 - Digital GND. 71 XTAI I Crystal oscillation circuit input. When the master clock is input externally, input it from this pin. | 50 | RFAC | ī | | EFM signal input. | | | | |
| FILO O Analog Master PLL filter output (slave = digital PLL). 54 FILI I Master PLL filter input. 55 PCO O 1, Z, 0 Master PLL charge pump output. 56 AV DD 1 Analog power supply. 57 BIAS I Asymmetry circuit constant current input. 58 VCTL I Wide-band EFM PLL VC02 control voltage input. 59 V16M I/O 1, 0 Wide-band EFM PLL VC02 oscillation output. Serves as wide-band EFM PLL clock input by switching with the command. 60 VPCO O 1, Z, 0 Wide-band EFM PLL charge pump output. 61 DVD02 Digital power supply. 62 ASYE I Asymmetry circuit on/off (low = oft, high = on). 63 MD2 I Digital Out on/oft control (low = off, high = on). 64 DOUT O 1, 0 Digital Out output. 65 LRCK O 1, 0 D/A interface. LR clock output. f = Fs 66 PCMD O 1, 0 D/A interface. Serial data output (two's complement, MSB first). 67 BCK O 1, 0 D/A interface. Bit clock output. 68 EMPH O 1, 0 Outputs a high signal when the playback disc has emphasis, and a low signal when there is no emphasis. 69 XTSL I Crystal selection input. Low when the crystal is 16.9344MHz; high when it is 33.8688MHZ. 70 DVSs2 - Digital GND. 71 XTAI I Crystal oscillation circuit input. When the master clock is input externally, input it from this pin. | 51 | AVss1 | - | - | Analog GND. | | | | |
| 54 FILI I Master PLL filter input. 55 PCO O 1, Z, 0 Master PLL charge pump output. 56 AV D 1 - Analog power supply. 57 BIAS I Asymmetry circuit constant current input. 58 VCTL I Wide-band EFM PLL VCO2 control voltage input. 59 V16M I/O 1, 0 Wide-band EFM PLL VCO2 oscillation output. Serves as wide-band EFM PLL clock input by switching with the command. 60 VPCO O 1, Z, 0 Wide-band EFM PLL charge pump output. 61 DV D 2 - Digital power supply. 62 ASYE I Asymmetry circuit on/off (low = oft, high = on). 63 MD2 I Digital Out on/oft control (low = off, high = on). 64 DOUT O 1, 0 Digital Out output. 65 LRCK O 1, 0 D/A interface. LR clock output. f = Fs 66 PCMD O 1, 0 D/A interface. Serial data output (two's complement, MSB first). 67 BCK O 1, 0 D/A interface. Bit clock output. 68 EMPH O 1, 0 Outputs a high signal when the playback disc has emphasis, and a low signal when there is no emphasis. 69 XTSL I Crystal selection input. Low when the crystal is 16.9344MHz; high when it is 33.8688MHZ. 70 DVss2 - Digital GND. 71 XTAI I Crystal oscillation circuit input. When the master clock is input externally, input it from this pin. | 52 | CLTV | ī | | Multiplier VCO1 control voltage input. | | | | |
| S5 PCO | 53 | FILO | 0 | Analog | Master PLL filter output (slave = digital PLL). | | | | |
| 56 AV DD 1 Analog power supply. 57 BIAS I | 54 | FILI | 1 | | Master PLL filter input. | | | | |
| 57 BIAS I Asymmetry circuit constant current input. 58 VCTL I Wide-band EFM PLL VC02 control voltage input. 59 V16M I/O 1, 0 Wide-band EFM PLL VC02 oscillation output. Serves as wide-band EFM PLL clock input by switching with the command. 60 VPCO O 1, Z, 0 Wide-band EFM PLL charge pump output. 61 DVbb2 Digital power supply. 62 ASYE I Asymmetry circuit on/off (low = off, high = on). 63 MD2 I Digital Out on/oft control (low = off, high = on). 64 DOUT O 1, 0 Digital Out output. 65 LRCK O 1, 0 D/A interface. LR clock output. f = Fs 66 PCMD O 1, 0 D/A interface. Serial data output (two's complement, MSB first). 67 BCK O 1, 0 D/A interface. Bit clock output. 68 EMPH O 1, 0 Outputs a high signal when the playback disc has emphasis, and a low signal when there is no emphasis. 69 XTSL I Crystal selection input. Low when the crystal is 16.9344MHz; high when it is 33.8688MHZ. 70 DVss2 - Digital GND. 71 XTAI I Crystal oscillation circuit input. When the master clock is input externally, input it from this pin. | 55 | PCO | 0 | 1, Z, 0 | Master PLL charge pump output. | | | | |
| 58 VCTL I Wide-band EFM PLL VC02 control voltage input. 59 V16M I/O 1, 0 Wide-band EFM PLL VC02 oscillation output. Serves as wide-band EFM PLL clock input by switching with the command. 60 VPCO O 1, Z, 0 Wide-band EFM PLL charge pump output. 61 DVbb2 - Digital power supply. 62 ASYE I Asymmetry circuit on/off (low = oft, high = on). 63 MD2 I Digital Out on/oft control (low = off, high = on). 64 DOUT O 1, 0 Digital Out output. 65 LRCK O 1, 0 D/A interface. LR clock output. f = Fs 66 PCMD O 1, 0 D/A interface. Serial data output (two's complement, MSB first). 67 BCK O 1, 0 D/A interface. Bit clock output. 68 EMPH O 1, 0 Outputs a high signal when the playback disc has emphasis, and a low signal when there is no emphasis. 69 XTSL I Crystal selection input. Low when the crystal is 16.9344MHz; high when it is 33.8688MHZ. 70 DVss2 - Digital GND. 71 XTAI I Crystal oscillation circuit input. When the master clock is input externally, input it from this pin. | 56 | AV DD 1 | - | - | Analog power supply. | | | | |
| 59 V16M I/O 1, 0 Wide-band EFM PLL VC02 oscillation output. Serves as wide-band EFM PLL clock input by switching with the command. 60 VPCO O 1, Z, 0 Wide-band EFM PLL clock input by switching with the command. 61 DVbD2 - - Digital Power supply. 62 ASYE I Asymmetry circuit on/off (low = oft, high = on). 63 MD2 I Digital Out on/oft control (low = off, high = on). 64 DOUT O 1, 0 Digital Out output. 65 LRCK O 1, 0 D/A interface. LR clock output. f = Fs 66 PCMD O 1, 0 D/A interface. Serial data output (two's complement, MSB first). 67 BCK O 1, 0 D/A interface. Bit clock output. 68 EMPH O 1, 0 Outputs a high signal when the playback disc has emphasis, and a low signal when there is no emphasis. 69 XTSL I Crystal selection input. Low when the crystal is 16.9344MHz; high when it is 33.8688MHZ. 70 DVss2 - Digital GND. 71 XTAI I Crystal oscillation circuit input. When the master clock is input exter | 57 | BIAS | ı | | Asymmetry circuit constant current input. | | | | |
| wide-band EFM PLL clock input by switching with the command. 60 VPCO O 1, Z, 0 Wide-band EFM PLL charge pump output. 61 DVpc2 - Digital power supply. 62 ASYE I Asymmetry circuit on/off (low = oft, high = on). 63 MD2 I Digital Out on/oft control (low = off, high = on). 64 DOUT O 1, 0 Digital Out output. 65 LRCK O 1, 0 D/A interface. LR clock output f = Fs 66 PCMD O 1, 0 D/A interface. Serial data output (two's complement, MSB first). 67 BCK O 1, 0 D/A interface. Bit clock output. 68 EMPH O 1, 0 Outputs a high signal when the playback disc has emphasis, and a low signal when there is no emphasis. 69 XTSL I Crystal selection input. Low when the crystal is 16.9344MHz; high when it is 33.8688MHZ. 70 DVss2 - Digital GND. 71 XTAI I Crystal oscillation circuit input. When the master clock is input externally, input it from this pin. | 58 | VCTL | ı | | Wide-band EFM PLL VC02 control voltage input. | | | | |
| 61 DVp02 - Digital power supply. 62 ASYE I Asymmetry circuit on/off (low = oft, high = on). 63 MD2 I Digital Out on/oft control (low = off, high = on). 64 DOUT O 1, 0 Digital Out output. 65 LRCK O 1, 0 D/A interface. LR clock output. f = Fs 66 PCMD O 1, 0 D/A interface. Serial data output (two's complement, MSB first). 67 BCK O 1, 0 D/A interface. Bit clock output. 68 EMPH O 1, 0 Outputs a high signal when the playback disc has emphasis, and a low signal when there is no emphasis. 69 XTSL I Crystal selection input. Low when the crystal is 16.9344MHz; high when it is 33.8688MHZ. 70 DVss2 - Digital GND. 71 XTAI I Crystal oscillation circuit input. When the master clock is input externally, input it from this pin. | 59 | V16M | 1/0 | 1, 0 | | | | | |
| 62 ASYE I Asymmetry circuit on/off (low = oft, high = on). 63 MD2 I Digital Out on/oft control (low = off, high = on). 64 DOUT O 1, 0 Digital Out output. 65 LRCK O 1, 0 D/A interface. LR clock output. f = Fs 66 PCMD O 1, 0 D/A interface. Serial data output (two's complement, MSB first). 67 BCK O 1, 0 D/A interface. Bit clock output. 68 EMPH O 1, 0 Outputs a high signal when the playback disc has emphasis, and a low signal when there is no emphasis. 69 XTSL I Crystal selection input. Low when the crystal is 16.9344MHz; high when it is 33.8688MHZ. 70 DVss2 - Digital GND. 71 XTAI I Crystal oscillation circuit input. When the master clock is input externally, input it from this pin. | 60 | VPCO | 0 | 1, Z, 0 | Wide-band EFM PLL charge pump output. | | | | |
| 63 MD2 I Digital Out on/oft control (low = off, high = on). 64 DOUT O 1, 0 Digital Out output. 65 LRCK O 1, 0 D/A interface. LR clock output. f = Fs 66 PCMD O 1, 0 D/A interface. Serial data output (two's complement, MSB first). 67 BCK O 1, 0 D/A interface. Bit clock output. 68 EMPH O 1, 0 Outputs a high signal when the playback disc has emphasis, and a low signal when there is no emphasis. 69 XTSL I Crystal selection input. Low when the crystal is 16.9344MHz; high when it is 33.8688MHZ. 70 DVss2 - Digital GND. 71 XTAI I Crystal oscillation circuit input. When the master clock is input externally, input it from this pin. | 61 | DV _{DD} 2 | - | - | Digital power supply. | | | | |
| 64 DOUT O 1, 0 Digital Out output. 65 LRCK O 1, 0 D/A interface. LR clock output. f = Fs 66 PCMD O 1, 0 D/A interface. Serial data output (two's complement, MSB first). 67 BCK O 1, 0 D/A interface. Bit clock output. 68 EMPH O 1, 0 Outputs a high signal when the playback disc has emphasis, and a low signal when there is no emphasis. 69 XTSL I Crystal selection input. Low when the crystal is 16.9344MHz; high when it is 33.8688MHZ. 70 DVss2 - Digital GND. 71 XTAI I Crystal oscillation circuit input. When the master clock is input externally, input it from this pin. | 62 | ASYE | 1 | | Asymmetry circuit on/off (low = oft, high = on). | | | | |
| 65 LRCK O 1, 0 D/A interface. LR clock output. f = Fs 66 PCMD O 1, 0 D/A interface. Serial data output (two's complement, MSB first). 67 BCK O 1, 0 D/A interface. Bit clock output. 68 EMPH O 1, 0 Outputs a high signal when the playback disc has emphasis, and a low signal when there is no emphasis. 69 XTSL I Crystal selection input. Low when the crystal is 16.9344MHz; high when it is 33.8688MHZ. 70 DVss2 - Digital GND. 71 XTAI I Crystal oscillation circuit input. When the master clock is input externally, input it from this pin. | 63 | MD2 | ı | | Digital Out on/oft control (low = off, high = on). | | | | |
| 66 PCMD O 1, 0 D/A interface. Serial data output (two's complement, MSB first). 67 BCK O 1, 0 D/A interface. Bit clock output. 68 EMPH O 1, 0 Outputs a high signal when the playback disc has emphasis, and a low signal when there is no emphasis. 69 XTSL I Crystal selection input. Low when the crystal is 16.9344MHz; high when it is 33.8688MHZ. 70 DVss2 - Digital GND. 71 XTAI I Crystal oscillation circuit input. When the master clock is input externally, input it from this pin. | 64 | DOUT | 0 | 1, 0 | Digital Out output. | | | | |
| 67 BCK O 1, 0 D/A interface. Bit clock output. 68 EMPH O 1, 0 Outputs a high signal when the playback disc has emphasis, and a low signal when there is no emphasis. 69 XTSL I Crystal selection input. Low when the crystal is 16.9344MHz; high when it is 33.8688MHZ. 70 DVss2 - Digital GND. 71 XTAI I Crystal oscillation circuit input. When the master clock is input externally, input it from this pin. | 65 | LRCK | 0 | 1, 0 | D/A interface. LR clock output. f = Fs | | | | |
| 68 EMPH O 1, 0 Outputs a high signal when the playback disc has emphasis, and a low signal when there is no emphasis. 69 XTSL I Crystal selection input. Low when the crystal is 16.9344MHz; high when it is 33.8688MHZ. 70 DVss2 - Digital GND. 71 XTAI I Crystal oscillation circuit input. When the master clock is input externally, input it from this pin. | 66 | PCMD | 0 | 1, 0 | D/A interface. Serial data output (two's complement, MSB first). | | | | |
| signal when there is no emphasis. 69 XTSL I Crystal selection input. Low when the crystal is 16.9344MHz; high when it is 33.8688MHZ. 70 DVss2 - Digital GND. 71 XTAI I Crystal oscillation circuit input. When the master clock is input externally, input it from this pin. | 67 | BCK | 0 | 1, 0 | D/A interface. Bit clock output. | | | | |
| 33.8688MHZ. 70 DVss2 - Digital GND. 71 XTAI I Crystal oscillation circuit input. When the master clock is input externally, input it from this pin. | 68 | EMPH | 0 | 1, 0 | | | | | |
| 71 XTAI I Crystal oscillation circuit input. When the master clock is input externally, input it from this pin. | 69 | XTSL | I | | Crystal selection input. Low when the crystal is 16.9344MHz; high when it is 33.8688MHZ. | | | | |
| input it from this pin. | 70 | DVss2 | - | - | Digital GND. | | | | |
| 72 XTAO O Crystal oscillation circuit output. | 71 | XTAI | 1 | | | | | | |
| | 72 | XTAO | 0 | | Crystal oscillation circuit output. | | | | |

| Pin NO. | Symbol | I/O | | Description |
|------------|--------|-----|------|--|
| 73 | SOUT | 0 | 1, 0 | Serial data output in servo block. |
| 74 | SOCK | 0 | 1, 0 | Serial data readout clock output in servo block. |
| 75 | XOLT | 0 | 1, 0 | Serial data latch output in servo block. |
| 76 | sqso | 0 | 1, 0 | Sub-Q 80-bit, PCM peak or level data outputs. CD TEXT data output. |
| 77 | SQCK | ı | | SQSO readout clock input. |
| 78 | SCSY | 1 | | GRSCOR resynchronization input. |
| 79 | SBSO | 0 | 1, 0 | Sub-Q P to W serial output. |
| 80 | EXCK | 1 | | SBSO readout clock input. |

- * PCMD is a MSB first, two's complement output.
- * GTOP is used to monitor the frame sync protection status. (High: sync protection window released.)

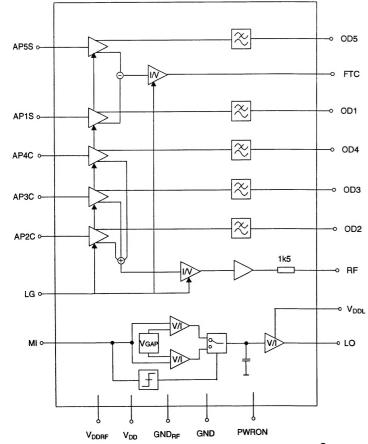
 * XUGF is the frame sync obtained from the EFM signal, and is negative pulse. It is the signal before sync protection.
- * XPCK is the inverse of the EFM PLL clock. The PLL is designed so that the falling edge and the EFM signal
- Atransition point coincide.
- * The GFS signal goes high when the frame sync and the insertion protection timing match.
- * RFCK is derived from the crystal accuracy, and has a cycle of 136us. (during normal speed)
- * C2PO represents the data error status.
- * XROF is generated when the 32K RAM exceeds the +-28F jitter margin.

Q501 : PCM1710



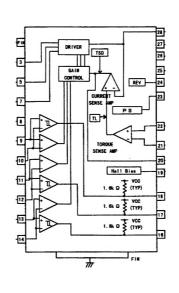
| PIN NAME | NUMBER | FUNCTION |
|--------------------|------------|---|
| Input Inte | rface Pins | |
| LRCIN | 1 | Sample Rate Clock Input. Controls the update rate (fs). |
| DIN | 2 | Serial Data Input. MSB first, right justified format contains a frame of 16-bit or 20-bit data. |
| BCKIN | 3 | Bit Clock Input. Clocks in the data present on DIN input. |
| Mode Cor | trois and | Clock Signals |
| CLKO | 4 | Buffered Output of Oscillator. Equivalent to fs. |
| XTI | 5 | Oscillator Input (External Clock Input). For an internal clock, tie XTI to one side of the crystal oscillator. For an external clock, tie XTI to the output of the chosen external clock. |
| хто | 6 | Oscillator Output. When using the internal clock, tie to the opposite side (from pin 5) of the crystal oscillator. When using an external clock, leave XTO open. |
| CKSL | 23 | System Clock Select. For 384fs, tie CKSL "High". For 256fs, tie CKSL "Low". |
| MODE | 24 | Operation Mode Select. For serial mode, tie MODE "High". For parallel mode, tie MODE "Low". |
| MUTE | 25 | Mute Control. To disable soft mute, tie MUTE "High". To enable soft mute, tie MUTE "Low". |
| MD/DM1 | 26 | Mode Control for Data/De-emphasis. See "Mode Control Functions" on page 11. |
| MC/DM2 | 27 | Mode Control for BCKIN/De-emphasis. See "Mode Control Functions" on page 11. |
| ML/DSD | 28 | Mode Control for WDCK/Double speed dubbing. See "Mode Control Functions" on page 11. |
| Analog Fu | inctions | |
| V _{OUT} R | 13 | Right Channel Analog Output. |
| V _{OUT} L | 16 | Left Channel Analog Output. |
| Power Su | ply Conne | ections |
| DGND | 7, 22 | Digital Ground. |
| V _{DD} | 8, 21 | Digital Power Supply (+5V). |
| V _{CC} 2R | 9 | Analog Power Supply (+5V), Right Channel DAC. |
| AGND2R | 10 | Analog Ground (DAC), Right Channel. |
| EXT1R | 11 | Output Amplifier Common, Right Channel. Bypass to ground with a 10µF capacitor. |
| EXT2R | 12 | Output Amplifier Bias, Right Channel. Connect to EXT1R. |
| AGND | 14 | Analog Ground. |
| V _{cc} | 15 | Analog Power Supply (+5V). |
| EXT2L | 17 | Output Amplifier Bias, Left Channel. Connect to EXT1L. |
| EXT1L | 18 | Output Amplifier Common, Left Channel. Bypass to ground with a 10μF capacitor. |
| AGND2L | 19 | Analog Ground (DAC), Left Channel. |
| V _{CC} 2L | 20 | Analog Power Supply (+5V), Left Channel DAC. |

Q101:TZA1022

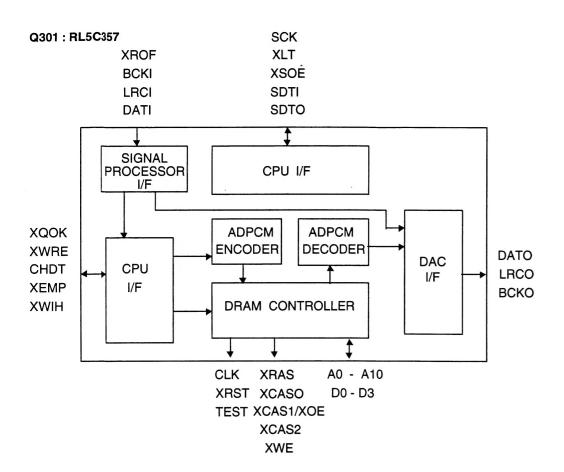


| SYMBOL | PIN | DESCRIPTION |
|-------------------|-----|---|
| OD2 | 1 | output photo diode amplifier 2 |
| OD3 | 2 | output photo diode amplifier 3 |
| OD4 | 3 | output photo diode amplifier 4 |
| OD5 | 4 | output photo diode amplifier 5 |
| OD1 | 5 | output photo diode amplifier 1 |
| PWRON | 6 | power on switch |
| RF | 7 | output data signal |
| V _{DDRF} | 8 | RF ampliPer supply voltage |
| V _{DD} | 9 | supply voltage |
| GND | 10 | ground |
| GND _{RF} | 11 | ground RF amplifier |
| V _{DDL} | 12 | laser supply voltage |
| LO | 13 | current output for the laser diode |
| МІ | 14 | Monitor input |
| n.c. | 15 | not connected |
| n.c. | 16 | not connected |
| AP1S | 17 | Input photo diode amplifier (satellite) |
| AP2C | 18 | Input photo diode amplifier (central) |
| n.c. | 19 | not connected |
| FTC | 20 | output fast track counting |
| LG | 21 | CD/CDRW gain switch |
| AP3C | 22 | Input photo diode amplifier (central) |
| AP5S | 23 | Input photo diode amplifier (satellite) |
| AP4C | 24 | Input photo diode amplifier (central) |

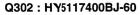
Q251: BA6856FP

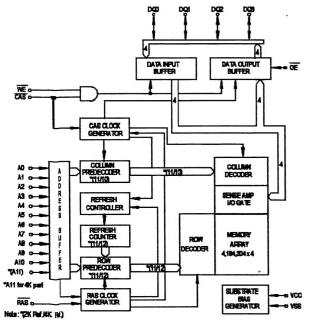


| PIN No | 端子名 | 機 解 |
|--------|-----------------------------|--|
| | /Pin Name | /Function |
| 1 | N.C. | N.C. |
| 2 | N.C. | N.C. |
| 3 | A ₃ | 出力罐子/Output3 for mator |
| 4 | N.C. | N.C. |
| 5 | A ₂ | 出力端子/Output2 for motor |
| 6 | N.C. | N.C. |
| 7 | A ₁ | 出力罐子/Output1 for motor |
| 8 | GND | GND端子/GND |
| 9 | H ₁ + | ホール信号入力端子/Hall input Amp1. positive input |
| 10 | H ₁ - | ホール信号入力端子/Hall input Amp1. negative input |
| 11 | H ₂ ⁺ | ホール信号入力端子/Hall input Amp2_ positive input |
| 12 | H ₂ | ホール信号入力端子/Hall input Amp2. negative input |
| 13 | H ₃ ⁺ | ホール信号入力端子/Hall input Amp3. positive input |
| 14 | H ₃ - | ホール信号入力端子/Hell input Amp3. negative input |
| 15 | N.C. | N.C. |
| 16 | FG3 | FG信号出力端子/FG3 signal output terminal |
| 17 | FG2 | FG信号出力端子/FG2 signal output terminal |
| 18 | FG1 | FG信号出力端子/FG1 signal output terminal |
| 19 | V _H | ホールバイアス端子/Hell Bias |
| 20 | C _{NF} | 位相補償用コンデンサ接続端子 |
| | | /Capacitor connection pin for phase compensation |
| 21 | E _{CR} | 出力電圧制御基準端子 |
| | | /Torque control standard voltage inpust terminal |
| 22 | Ec | 出力電圧制御端子/Torque control voltage input terminal |
| 23 | PS | パワーセーブ端子/ POWER SAVE switch |
| 24 | R _{ev} | 逆転端子/Reverse terminal |
| 25 | V _{cc} | 電源端子/Power supply for signal division |
| 26 | V _{M2} | 12V用電源端子/Power supply2 for duriver |
| 27 | V _{M1} . | モータ電源端子/Power supply! for draver |
| 28 | R | 出力電流検出用抵抗接続端子 |
| | | /Power supply for driver division |
| FIN | FIN | GND |

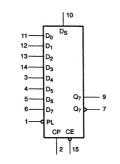








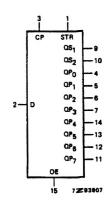
QU57 QU58: 74HC165

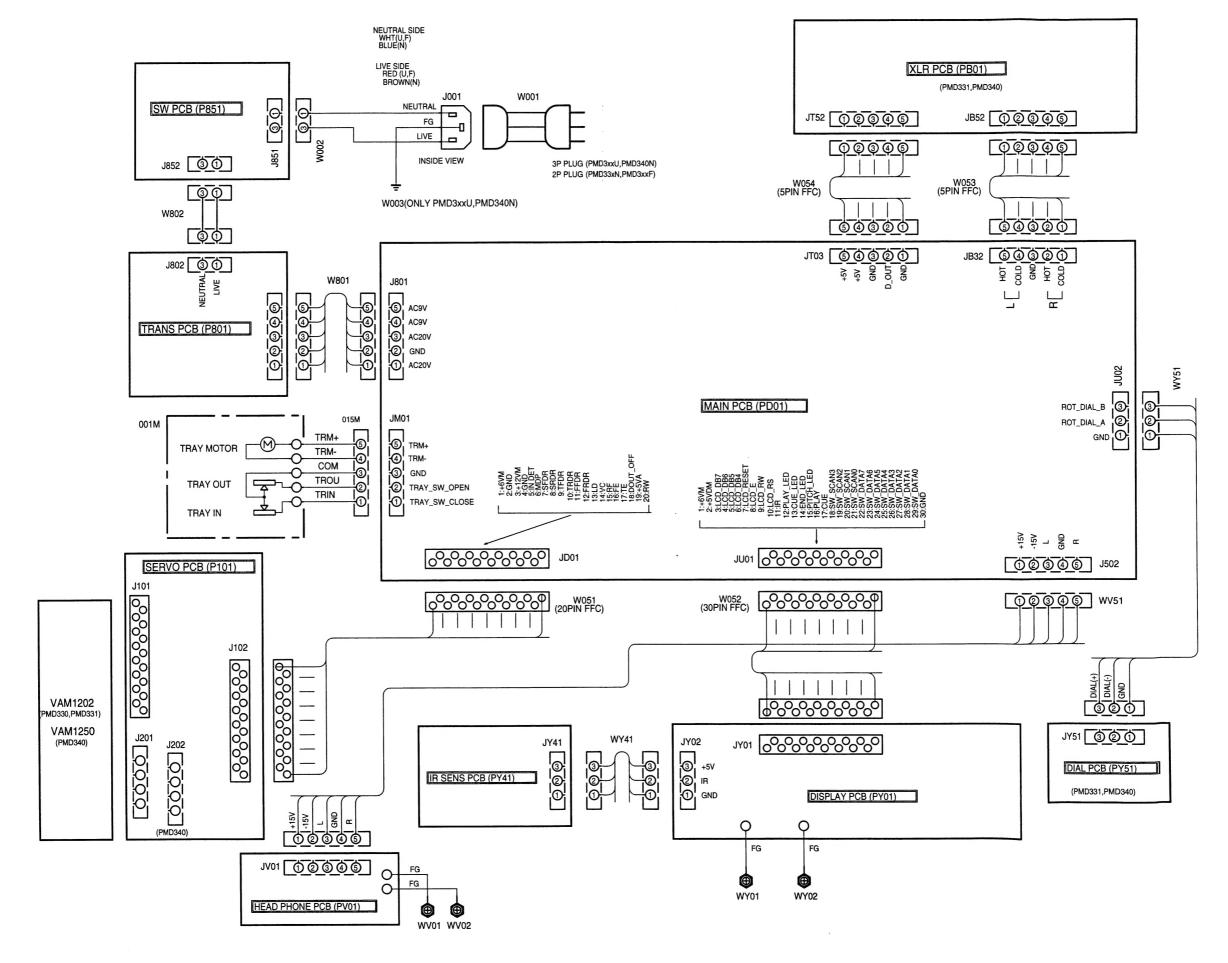


| PIN NUMBER | SYMBOL | FUNCTION |
|----------------------------|----------------------------------|---|
| 1 | PL | Asynchronous parallel load input (active LOW) |
| 2 | СР | Clock input (LOW to HIGH, edge-triggered) |
| 7 | \overline{Q}_7 | Complementary output from the last stage |
| 8 . | GND | Ground (0 V) |
| 9 | Q ₇ | Serial output from last stage |
| 10 | D _S | Serial data input |
| 11, 12, 13, 14, 3, 4, 5, 6 | D ₀ to D ₇ | Parallel data inputs |
| 15 | CE | Clock enable input (active LOW) |
| 16 | V _{CC} | Positive supply voltage |

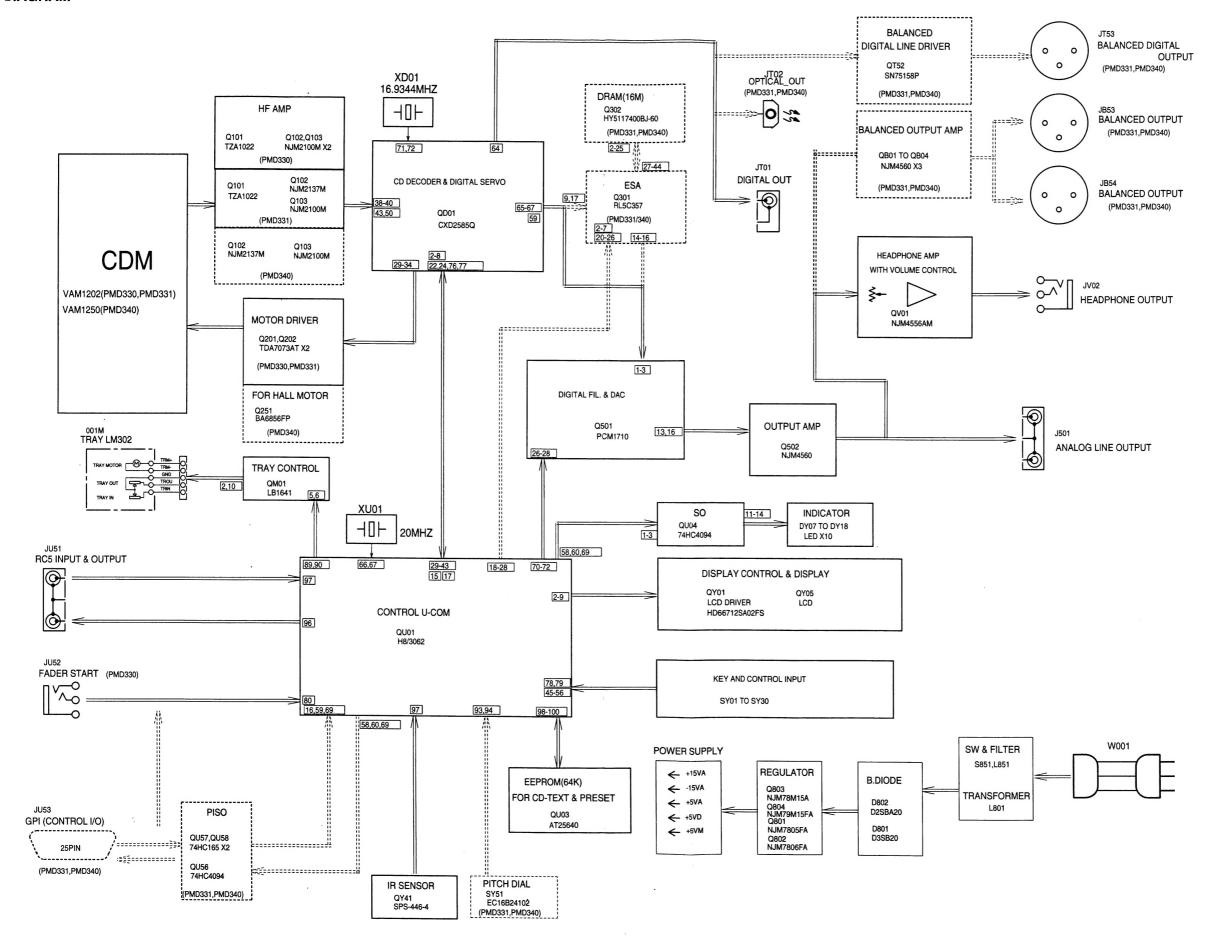
QU04 QU56: 74HC4094

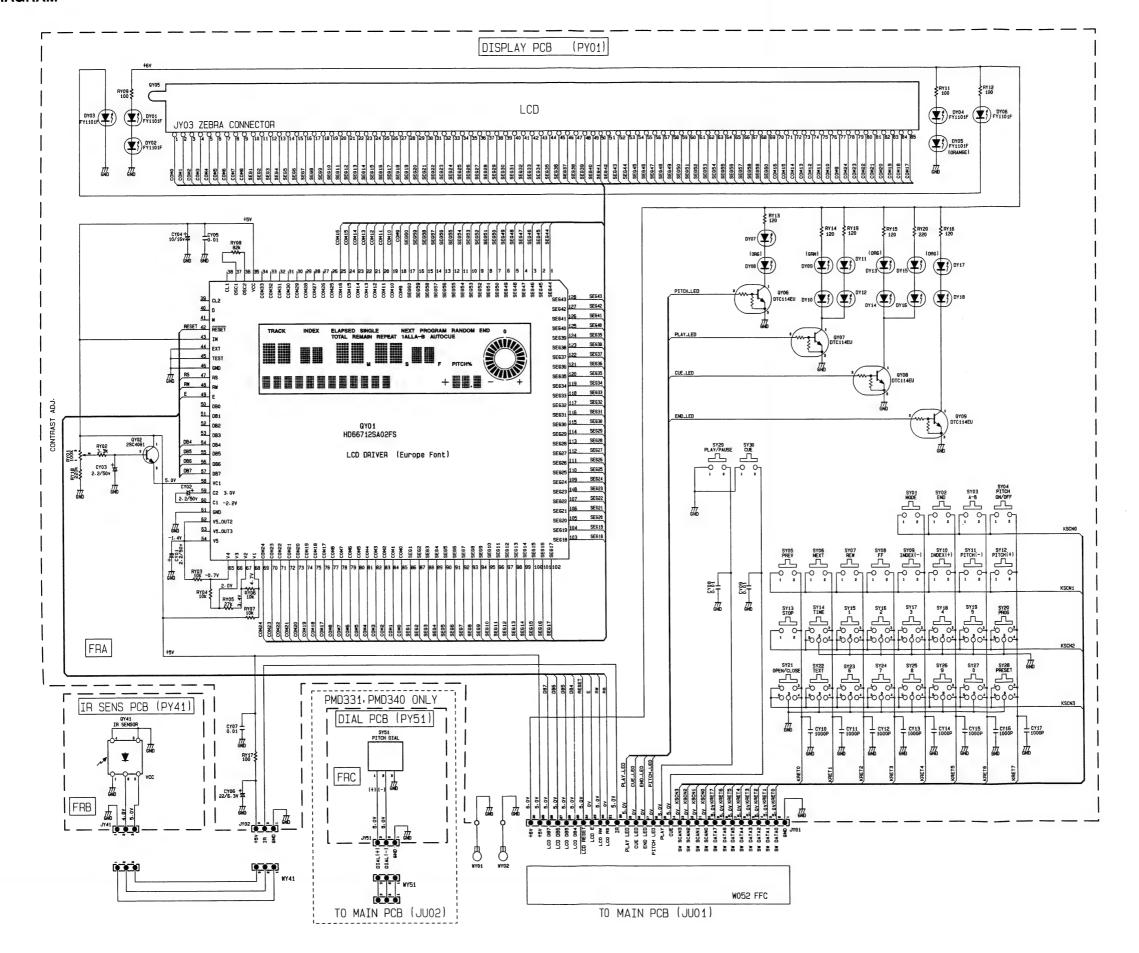
| PIN NO. | SYMBOL | NAME AND FUNCTION |
|---------------------------|------------------------------------|-------------------------|
| 1 | STR | strobe input |
| 2 | D | serial input |
| 3 | СР | clock input |
| 4, 5, 6, 7,14, 13, 12, 11 | QP ₀ to QP ₇ | parallel outputs |
| 8 | GND | ground (0 V) |
| 9, 10 | QS ₁ ,Q S ₂ | serial outputs |
| 15 | OE | output enable input |
| 16 | V _{CC} | positive supply voltage |

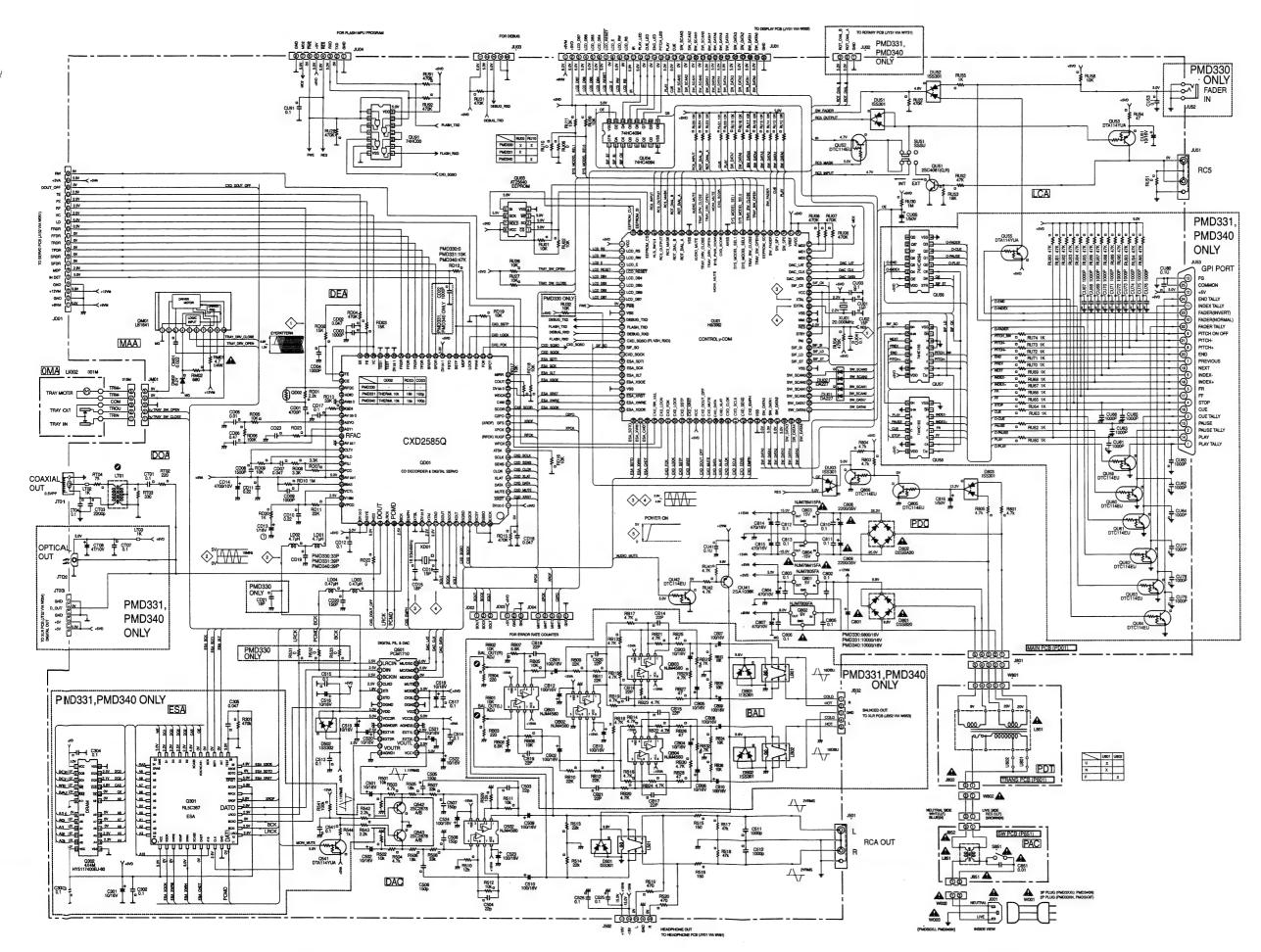


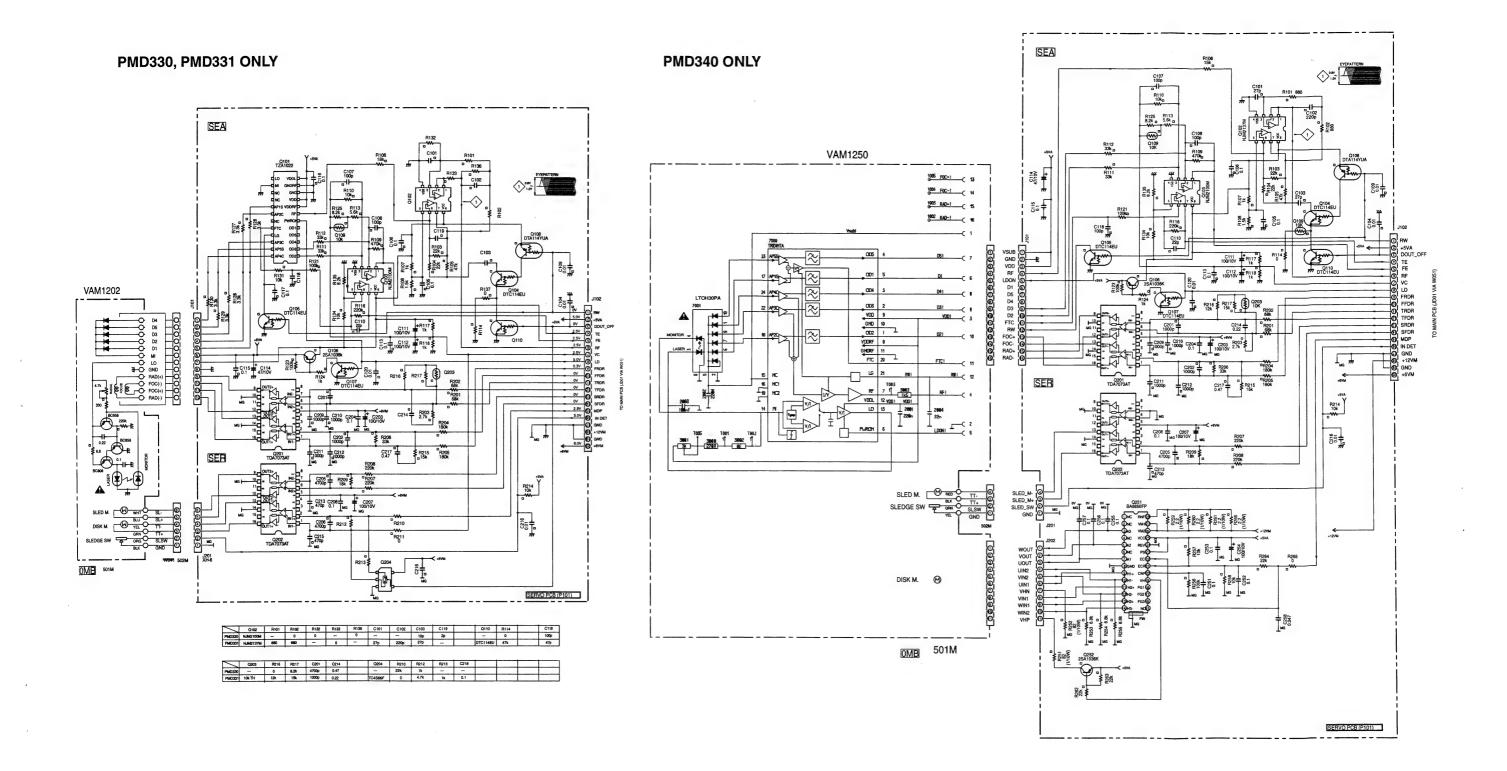


7. BLOCK DIAGRAM

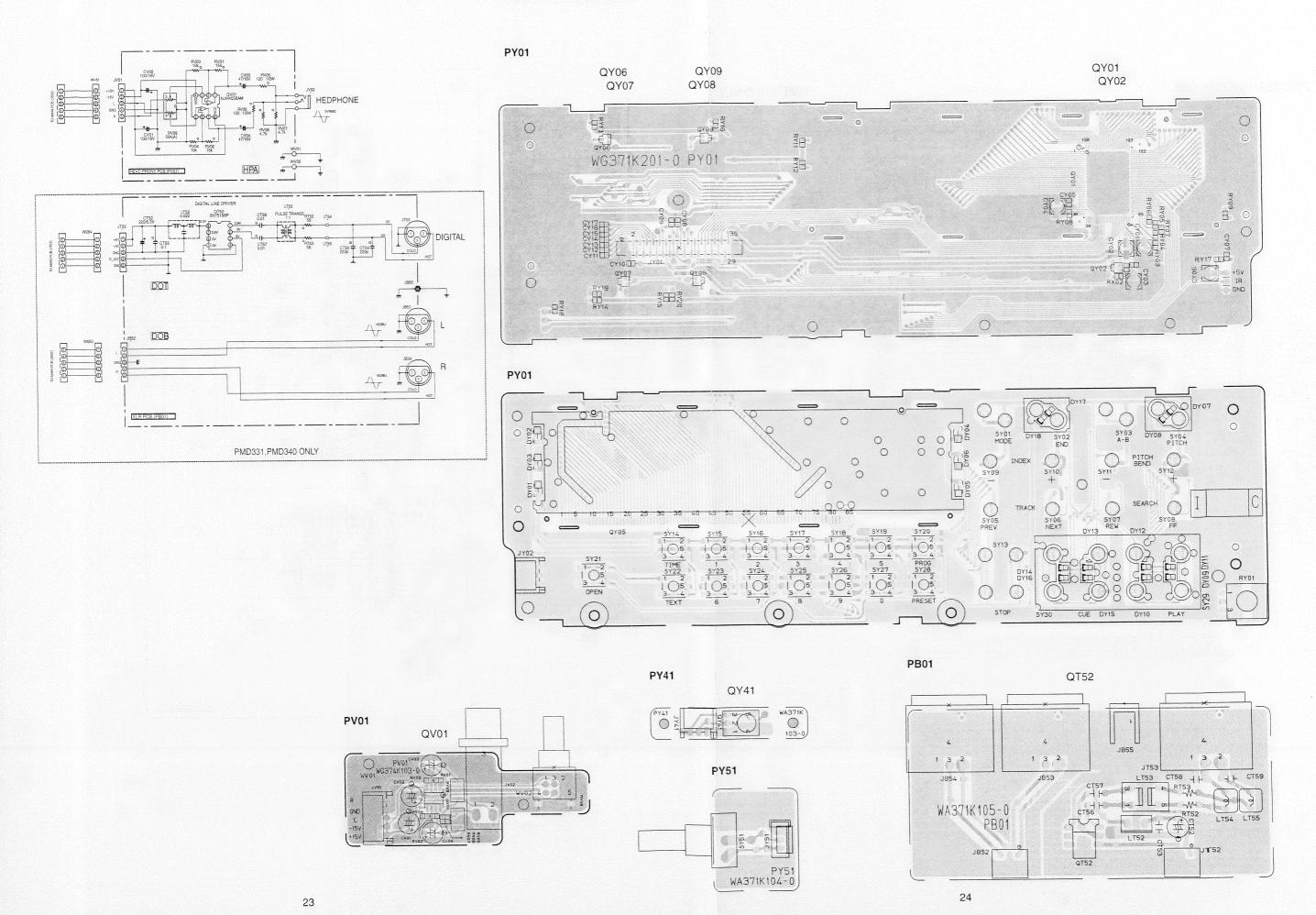








9. PARTS LOCATION



PD01 Q802 Q801 Q301 Q302 QU04 QD01 Q804 QM01 QU41 QU42 Q501 Q541 Q502 QB03 QB02 QB04 QU01 Q803 QB01 QU91 QU03 Q806 Q805 QU58 QU57 QU56 QU52 QU51 QU64 QU63 QU62 Q61 QU60 QU59 0 # #

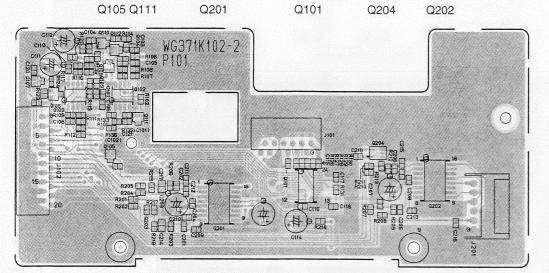
05130

0

26

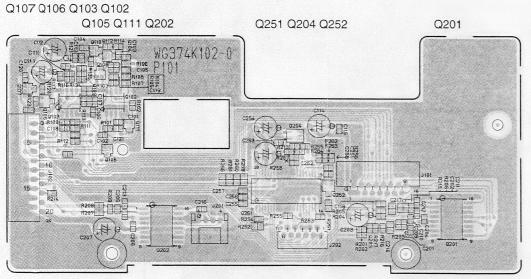
P101 (PMD330, PMD331)

Q110 Q108 Q112 Q107 Q106 Q103 Q102

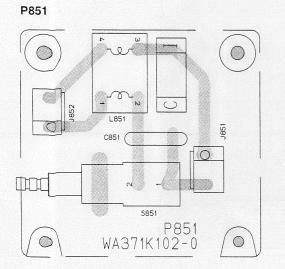


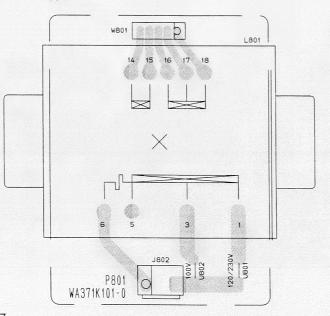
P101 (PMD340)

Q110 Q108 Q112





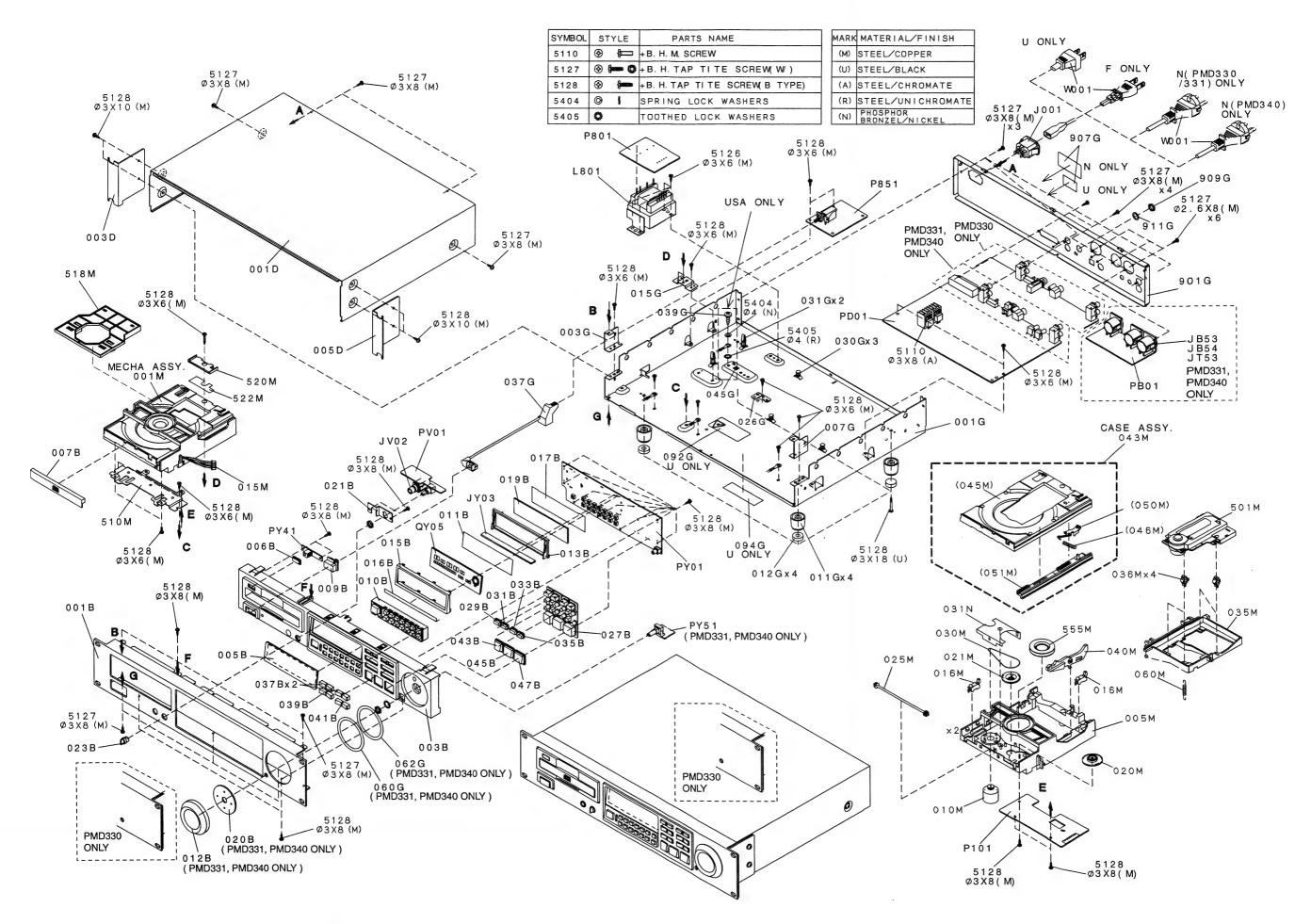




10. EXPLODED VIEW AND PARTS LIST

| POS. NO | VERS. COLOR | PART NO. (FOR PCS) | DESCRIPTION | PART NO. (MJI) | POS. NO | VERS. COLOR | PART NO. (FOR PCS) | DESCRIPTION | PART NO. (MJI) |
|--------------|-----------------------|-----------------------|--|--|---------------|----------------|----------------------------------|--|------------------------------------|
| 001B | BLACK | | FRONT PANEL PMD330 BLK | 371K248010 | 062G | | | STICKER | *** |
| 001B | GRAY | 9965 000 01604 | FRONT PANEL PMD330 GRAY | 371K248020 | 092G | | | INNER LASER CAUTION | *** |
| | | 9900 000 01004 | [12] | | | | | | *** |
| 001B | BLACK | | FRONT PANEL PMD331 BLK | 371K248110 | 094G | | | LABEL LASER CAUTION | |
| 001B | 2013 A 2014 A 12 A 18 | 9965 000 01726 | FRONT PANEL PMD331 GRAY | 371K248120 | 901G | | | REAR PANEL | *** |
| 001B | BLACK | | FRONT PANEL PMD340 BLK | 371K248210 | 907G | | | LABEL | *** |
| 001B | GRAY | 9965 000 01721 | FRONT PANEL PMD340 GRAY | 371K248220 | 909G | | | B.H. TAP. SCREW | *** |
| | | 0000 000 01721 | | | 911G | | | PH.TAP.SCREW | *** |
| 003B 003B | BLACK | 9965 000 01605 | FRONT CHASSIS PMD330 BLK FRONT CHASSIS PMD330 GRAY | 371K105020 371K105030 | 001M | | 9965 000 01625 | NEW LOADER LM302 OLD TRAY BLK | 305K304680 |
| 003B | BLACK | | FRONT CHASSIS BLACK | 371K105040 | 005M | | 4822 464 10054 | FRAME K MOTOR | 305K401500 MM0030002 |
| 003B | GRAY | 9965 000 01722 | PMD331/N,/U, PMD340/N FRONT CHASSIS PMD331/340 | 371K105050 | 010M 015M | | 4822 361 21741 4822 321 63208 | CABLE | YB0038059 |
| | | | GRAY | | 016M | | 4822 271 30873 | MINI SWITCH | SM0102062 |
| 003B | 340/U | | FRONT CHASSIS PMD340 USA | 371K105060 | 020M | | 4822 522 33521 | GEAR | 305K05803 |
| 0000 | 0.00 | | BLACK | 07 111100000 | 021M | | 4822 528 81537 | PULLEY | 305K26201 |
| | | | BLACK | | | | | | 305K05850 |
| | | | | | 025M | | 4822 522 33522 | GEAR K | |
| 005B | BLACK | | WINDOW BLK | 371K158010 | 030M | | 4822 358 31325 | BELT | 305K26401 |
| 005B | GRAY | 9965 000 01606 | WINDOW GRAY | 371K158020 | 031M | | 4822 459 50976 | MASK | 305K30301 |
| 006B | | 9965 000 01607 | IR LENS | 371K355020 | 035M | | 4822 443 51265 | CASE | 305K06411 |
| 007B | BLACK | | ESCUTCHEON | 292K063220 | 036M | | 9965 000 01626 | SUSPENSION | 371K05601 |
| | 225 September 114 | | | | | | | | 305K00205 |
| 007B | 340/U | | ESCUTCHEON PMD340 USA | 292K063260 | 040M | | 4822 402 11212 | NEW LIFT ARM | |
| 007B | | 9965 000 01608 | ESCUTCHEON | 292K063230 | 043M | | 9965 000 00234 | CASE K NEW GEAR/OLD TRAY | 305K06460 |
| 009B | BLACK | | POWER BUTTON | 371K270150 | 045M | | | CASE BLACK | 305K06401 |
| 009B | GRAY | 9965 000 01609 | POWER BUTTON | 371K270160 | 046M | | | SPRING | 305K11501 |
| 010B | BLACK | | BUTTON | 371K270170 | 050M | | | CAM | 305K05401 |
| 010B | 150 (100 50 10) | 9965 000 01610 | BUTTON | 371K270180 | 051M | | | NEW SLIDER GEAR | 305K05805 |
| | IGNAT | 9900 000 01010 | | | | | 1000 100 00105 | | 305K11502 |
| 011B 012B | BLACK | | ROTARY KNOB PMD331/340 | *** 372K154010 | 060M 501M | 330,331 | 4822 492 33495 9965 000 01627 | SPRING MECHANISM VAM 1202 | 371K30450 |
| 012B | GRAY | 9965 000 01723 | BLACK ROTARY KNOB PMD331/340 | 372K154020 | 501M | 340 | 9965 000 01724 | 9305 022 20200 MECHANISM VAM 1250 | 374K30450 |
| 013B | | | GRAY LCD SPACER | *** | 510M | | | 9305 022 25001 BRACKET | *** |
| 015B | | | LCD HOLDER | *** | 518M | | | STOPPER FOR DISC | 371K11401 |
| | | | | | | | | 나는 물건 하게 많은 것이 없는 것이 없는 것이 없는 것이 없는 것이 없는 것이 없는 것이 없다. | 371K11402 |
| 016B | | | INSULATOR FOR 015B | *** | 520M | | | STOPPER FOR DISC | |
| 017B | | | LCD REFLECTOR | *** | 522M | | | ADHESIVE FOR 520M | 371K12201 |
| 019B | | | LCD LENS | *** | 555M | 330,331 | | CLAMPER ASSY VAM1202 | 371K0055 |
| 020B | | | WEIGHT FOR KNOB | *** | 555M | 340 | 9965 000 01725 | CLAMPER ASSY VAM1250 | 374K0055 |
| 021B | | | BRACKET FOR PHONE JACK | *** | | | | | |
| 023B | BLACK | | KNOB FOR PHONE VOLUME | 284T154310 | A J001 | /F | 9965 000 01313 | JACK 2P AC INLET M1910-H | YJ0400244 |
| | | 0005 000 04044 | | | | | | | YJ0400244 |
| 023B | GRAY | 9965 000 01611 | KNOB FOR PHONE VOLUME | 284T154050 | A J001 | /N | 9965 000 01313 | JACK 2P AC INLET M1910-H | Reference to the first term of the |
| 027B | | 9965 000 01612 | BUTTON RUBBER | 371K270010 | ▲ J001 | /U | | JACK 3P AC INLET M1910-D | YJ040024 |
| 029B | | 9965 000 01614 | BUTTON MODE KEY | 371K270020 | | | | | |
| 031B | | 9965 000 01615 | BUTTON END KEY | 371K270030 | W051 | | 9965 000 01602 | JUMPER LEAD 20P FFC | YU201705 |
| 033B | | 9965 000 01616 | BUTTON A-B KEY | 371K270040 | W052 | | 9965 000 01603 | JUMPER LEAD 30P FFC | YU301705 |
| | | | | | | 004 040 | | | YU050905 |
| 035B | | 9965 000 01617 | BUTTON PITCH KEY | 371K270050 | | 331,340 | | JUMPER LEAD 5P FPC | |
| 037B | | 9965 000 01618 | BUTTON +/- KEY | 371K270070 | W054 | 331,340 | | JUMPER LEAD 5P FPC | YU050905 |
| 039B | | 9965 000 01619 | BUTTON NEXT/PREV KEY | 371K270080 | | | | | 1 |
| 041B | | 9965 000 01620 | BUTTON FF/FR KEY | 371K270090 | | | | PACKING | |
| 043B | | 9965 000 01621 | BUTTON STOP KEY | 371K270100 | 001T | /F | | USER MANUAL | 371K8511 |
| 045B | | 9965 000 01622 | BUTTON CUE KEY | | | | 0005 000 01600 | | 371K8513 |
| | | | | 371K270110 | 001T | | 9965 000 01628 | USER MANUAL | |
| 047B | | 9965 000 01623 | BUTTON PLAY KEY | 371K270120 | 001T | /U | | USER MANUAL | 371K8512 |
| | | | | | A W001 | /F | | MAINS CORD 2P 12A 125V | ZC020011 |
| 001D | /U1B | | LID TOP COVER BLACK | 292J257030 | A W001 | 330/N | 4822 321 11439 | MAINS CORD 2P 10A 250V | ZC018030 |
| 003D | /U1B | | MOUNT BRACKET L | 371K160040 | | 331/N | | CLASS2 | |
| 005D | /U1B | | MOUNT BRACKET R | 371K160050 | ▲ W001 | | 4822 | MAINS CORD 3P 10A 250V | ZC02003 |
| 001G | | | CHASSIS | *** | ▲ W001 | /U | | MAINS CORD 3P 10A 125V | ZC02002 |
| 003G | | | SIDE BRACKET L | *** | | | | | |
| 007G | | | SIDE BRACKET R | *** | | | | | |
| 011G | | 9965 000 01624 | LEG BLACK | 371K057010 | | | | | |
| | | 3303 000 01024 | | | | | | | |
| 012G | | | BUFFER FOR LEG | 371K056020 | | | | 1 | |
| 015G | | | LOADER BRACKET | *** | | | | NOT STANDARD | |
| 026G | | | BRACKET FOR PD01 | *** | | | | SPEAR PARTS | |
| 030G | | | SUPPORT FOR PD01 | *** | 001S | 330 | | PACKING CASE PMD330 | 371K801 |
| 031G | | | SUPPORT FOR P801 | *** | 0015 | | | PACKING CASE PMD331 | 372K801 |
| | | 4000 400 40040 | | | | | | | |
| 037G | | 4822 402 10913 | LINK | 318K121010 | 001S | 340 | | PACKING CASE PMD340 | 374K801 |
| 039G | /U | | SCREW FOR GND | *** | 003S | | | CUSHION | 371K809 |
| | | | | In the second se | | 1 0 1 | | LAUDIO CADI E | ZD01000 |
| 045G | | | LABEL FOR GND | *** | W004 | 1/0 | | AUDIO CABLE | 2001000 |

NOTE: *** = PART IS LISTED FOR REFERENCE ONLY, MARANTZ WILL NOT SUPPLY THESE PARTS.



11. TECHNICAL DESCRIPTION

1. RC5 Code

This product is able to communicate to the other MARANTZ products with the RCA Pin cable.

1, RC5 □ - F

RCA Pinコードを介して他の機器と通信できる。

| | Command Name | Code | | Decod | le . | | 8 | nary | |
|--------|-------------------|---------|-----|-------|------|-------|-------|--------|--------|
| | | | SYS | | DATA | START | SYS | COM | DATA |
| | Play | 2053 | 20 | 53 | | 11 | 10100 | 110101 | |
| | Pause | 2048 | 20 | 48 | | 11 | 10100 | 110000 | |
| | Cue | 2059-10 | 20 | 59 | 10 | 11 | 10100 | 111011 | 1010 |
| | Stop | 2054 | 20 | 54 | | 11 | 10100 | 110110 | |
| | FF | 2052 | 20 | 52 | | 11 | 10100 | 110100 | |
| | FR(REW) | 2050 | 20 | 50 | | 11 | 10100 | 110010 | |
| | Index+ | 2034 | 20 | 34 | | 11 | 10100 | 100010 | |
| | Index- | 2035 | 20 | 35 | | 11 | 10100 | 100011 | |
| | Next | 2032 | 20 | 32 | | 11 | 10100 | 100000 | |
| | Previous | 2033 | 20 | 33 | | 11 | 10100 | 100001 | |
| | Pitch+ | 2038 | 20 | 38 | | 11 | 10100 | 100110 | |
| | Pitch- | 2039 | 20 | 39 | | 11 | 10100 | 100111 | |
| | Pitch Bend+ ★2 ★3 | 2038-10 | 20 | 38 | 10 | 11 | 10100 | 100110 | 1010 |
| | Pitch Bend- +2 +3 | 2039-10 | 20 | 39 | 10 | 11 | 10100 | 100111 | 1010 |
| | A-B | 2059 | 20 | 59 | | 11 | 10111 | 111011 | |
| | Program | 2041 | 20 | 41 | | 11 | 10100 | 101001 | |
| NPOT | Pitch On/Off | 2037 | 20 | 37 | | 11 | 1010 | 100101 | |
| Ž | Open/Close | 2045 | 20 | 45 | | 11 | 10100 | 101101 | |
| | Time | 2011 | 20 | 11 | | 11 | 10100 | 1011 | |
| | Mode | 2036-10 | 20 | 36 | 10 | 11 | 10100 | 100100 | 1010 |
| | Preset | 2041-12 | 20 | 41 | 12 | 11 | 10100 | 101001 | 1100 |
| | END monitor | 2043-10 | 20 | 43 | 10 | 11 | 10100 | 101011 | 1010 |
| | CD-TEXT | 2088 | 20 | 88 | | 10 | 10100 | 11000 | |
| | 0 | 2000 | 20 | 00 | | 11 | 10100 | 0 | |
| | 11 | 2001 | 20 | 01 | | 11 | 10100 | 1 | |
| | 2 | 2002 | 20 | 02 | | 11 | 10100 | 10 | - |
| | 3 | 2003 | 20 | 03 | | 11 | 10100 | 11 | |
| | 4 | 2004 | 20 | 04 | | 11 | 10100 | 100 | |
| | 5 | 2005 | 20 | 05 | | 11 | 10100 | 101 | |
| | 6 | 2006 | 20 | 06 | | 11 | 10100 | 110 | |
| | 7 | 2007 | 20 | 07 | | 11 | 10100 | 111 | |
| | 8 | 2008 | 20 | 08 | | 11 | 10100 | 1000 | |
| | 9 | 2009 | 20 | 09 | | 11 | 10100 | 1001 | |
| | SERVICE *1 | 166363 | 16 | 63 | 63 | 11 | 10000 | 111111 | 111111 |
| OUTPUT | Connect | 1856 | 18 | 56 | | 11 | 10010 | 111000 | |
| 5 | Disconnect | 1857 | 18 | 57 | | 11 | 10010 | 111001 | |

- *1 The service code is available during STOP mode only.
- *2 The Pitch Bend+ and Pitch Bend- are not available with the digital out on.
- *3 The Pitch Bend+ and Pitch Bend- are not available on PMD330.
- *1 サービスコードはSTOP状態の時のみ受け付ける。
- *2 Pitch Bend+、Pitch Bend- はデジタルアウト On 時には受け 付けない。
- *3 表内の Pitch Bend+、Pitch Bend-は PMD330では No Action とする。

5. GPI code

The GPI code is input from external controller with D-Sub 25 Pin connector.

2, GPI コード

D-Sub25Pinコネクタで外部のコントローラーより入力される。

| Pin | Name | 1/0 | Active |
|-----|-------------------|-----|--------|
| 1 | PLAY TALLY | 0 | Low |
| 2 | PAUSE TALLY | 0 | Low |
| 3 | CUE TALLY | 0 | Low |
| 4 | STOP | - | Low |
| 5 | FR | - | Low |
| 6 | INDEX- | | Low |
| 7 | PREVIOUS | 1 | Low |
| 8 | PITCH+ | 1 | Low |
| 9 | PITCH ON/OFF | | Low |
| 10 | FADER(NORMAL) | - 1 | Low |
| 11 | INDEX #2/#3 TALLY | 0 | Low |
| 12 | +5V | - | |
| 13 | FG COMMON | - | |
| 14 | PLAY | | Low |
| 15 | PAUSE | - 1 | Low |
| 16 | CUE | - 1 | Low |
| 17 | FF | | Low |
| 18 | INDEX+ | 1 | Low |
| 19 | NEXT | ı | Low |
| 20 | END | | Low |
| 21 | PITCH- | 1 | Low |
| 22 | FADER TALLY | 0 | Low |
| 23 | FADER(INVERT) | | High |
| 24 | END TALLY | 0 | Low |
| 25 | TALLY COMMON | - | |

- * The fader start is on during PLAY, and off during PAUSE.
- * The index is output by pulse signal.

When the index #2 is selected, the pulse signal of 200ms is output at the top of index #2.

200ms

When the index #3 is selected, the pulse signal of 200ms is output at the top of index #3.

200ms

3. Double spped Reading

The disc (spindle) motor of PMD331/340 rotates at double speed for the Instant start & Anti-shock (shockproof) behavior function.

The data that is read out at double speed from a CD is put into the shockproof memory control & DRAM.

The data that is in the shockproof memory is forwarded to the DAC and is played back at normal speed.

When it is set Digital Out to "ON" on the preset menu, the disc (spindle) motor rotates at normal speed, and the data is read at normal speed.

Therefore, Digital output is always outputted at normal speed.

PMD330 doesn't have the shockproof memory control. Therefore the disc (spindle) motor of PMD330 always rotates at normal speed.

※ Fader Start は、PlayでOn し、Pause 状態でOff となる。 ※ Indexはパルスで出力する。

Index#2選択時、Index#2の頭で200msのパルスを出力する。

11112222222

200ms

Index#3選択時、Index#3の頭で200msのパルスを出力する。 2223333333

200ms

3. 2倍速について

PMD331/340は、Instant start & Anti-shock (shokproof) behavior 機能の為に、2倍速でDisc (Spindle) Motor を回転させています。

CDから2倍速にて読み出されたデータは、Shokproof memory control & DRAMにいったんメモリーします。

ここでメモリーされたデータは1倍速にてShokproof memory control よりDACに転送され通常のスピードで再生されます。 但し、Preset MenuにてDigital Out "ON"に設定した場合は、 1倍速でDisk (Spindle) Motor は回転し1倍速でデーターの 読み出しが行われます。

よって、Digital 出力は常に1倍速で出力されます。

PMD330はShokproof memory controlを搭載していないので常に Disc (Spindle) Motor は1倍速にて回転します。

12. ELECTRICAL PARTS LIST

ASSIGNMENT OF COMMON PARTS CODES. RESISTORS R**: 1) GD05×××140, Carbon film fixed resistor, ±5% 1/4W $\overline{R***}$: 2) GD05 ××× 160, Carbon film fixed resistor, ±5% 1/6W 1)-Resistance value Examples: Resistance value 1 k Ω 102 100 k Ω 104 $10~\Omega~....~100$ 0.1 Ω 001 $18~\Omega~....~180~~2.7~k\Omega~....~272~~680~k\Omega~....~684$ $0.5\,\Omega\dots005$ $100 \ \Omega \ \ 101$ $390 \ \Omega \ \ 391$ $1~\Omega\,....~010$ 10 kΩ 103 1 MΩ 105

Note: Please distinguish 1/4W from 1/6W by the shape of parts

22 kΩ 223 4.7 MΩ 475

CAPACITORS

 $6.8\,\Omega\dots068$

used actually.

```
C***: CERAMIC CAP.
        3) DD1 \times \times \times \times 370,
                                 Ceramic capacitor
                                 Disc type
               2 3
                                 Temp.coeff.P350 ~N1000, 50V
                            Capacity value
                            Tolerance
 Examples;
    2 Tolerance (Capacity deviation)
```

±0.25 pF 0

±0.5 pF 1 ±5% 5

* Tolerance of COMMON PARTS handled here are as follows:

0.5 pF \sim 5 pF ±0.25 pF 6 pF∼ 10 pF ±0.5 pF 12 pF∼ 560 pF ±5%

③ Capacity value 0.5 pF 005 3 pF 030 100 pF 101 1 pF 010 10 pF 100 220 pF 221 1.5 pF 015 47 pF 470 560 pF 561

C*** : CERAMIC CAP. High dielectric constant ceramic 4) DK16××× 300, capacitor 4 Disc type Temp.chara. 2B4, 50V Capacity value

Examples; 4 Capacity value

100 pF 101 1000 pF 102 10000 pF 103 470 pF 471 2200 pF 222

C***: 5) ELECTROLY CAP.(本), 6) FILM CAP.(中) 5) EA××××××10, Electrolytic capacitor One-way lead type, Tolerance ±20% 6 Working voltage

Examples ; ⑤ Capacity value

100 μF107 330 μF337 1100 μF118 $0.1~\mu F \dots 104$ 4.7 μF 475 10 μF 106 22 μF 226 $0.33 \, \mu F \dots 334$ 1 μF 105 2200 µF 228

Capacity value

Working voltage

25V 025 6.3V 006 35V 035 10V 010 16V 016 50V 050

6) DF15×××350 T DF15×××310 T → Plastic film capacitor One-way type, Mylar ±5% 50V Plastic film capacitor DF16 $\times\times\times310$ One-way type, Mylar ±10% 50V

Capacity value

Examples; 7

| Capacity value | | |
|--------------------|-------|-------------|
| 0.001 µF (1000 pF) |) 102 | 0.1 μF 104 |
| 0.0018 μF | 182 | 0.56 μF 564 |
| 0.01 μF | 103 | 1 μF 105 |
| 0.015 uE | 153 | |

NOTE : 1) The above CODES (R***, R***, C***, C*** and C ***) are omitted on the schematic diagram in some case.

2) On the occasion, be confirmed the common parts on the parts list.

3) Refer to "Common Parts List" for the other common parts (RI05, DD4, DK4).

NOTE ON SAFETY FOR FUSIBLE RESISTOR:

The suppliers and their type numbers of fusible resistors are as follows; 1. KOA Corporation Type No. (KOA) Description Part No. (MJI) $RF25S \times \times \times \times \Omega J$ (±5% 1/4W) $NH05 \times \times \times 140$ NH05 ××× 120 -→ RF50S ××××ΩJ (±5% 1/2W) NH85 ××× 110 — → RF73B2A ×××× ΩJ (±5% 1/10W) NH95 $\times \times \times$ 140 \longrightarrow RF73B2E $\times \times \times \times \times \Omega$ J (±5% 1/4W) * Resistance value Resistance value $(0.1 \Omega - 10 k\Omega)$ 2. Matsushita Electronic Components Co., Ltd Description Part No. (MJI) Type No. (MEC)

NF05 × × × 140 ERD-2FCJ ××× (±5% 1/4W) RF05 ××× 140 ERD-2FCG × × × (±2% 1/4W) NF02×××140 RF02 × × × 140 -– * Resistance value * Resistance value

Examples:

* Resistance value $0.1\,\Omega\,....\,001$ 10 Ω 100 1 kΩ 102 100 kΩ 104 0.5 Ω 005 $18\;\Omega\;....\;180$ $2.7 \text{ k}\Omega \dots 272 680 \text{ k}\Omega \dots 684$ 1 Ω 010 $100~\Omega~....~101$ $10 \text{ k}\Omega \dots 103$ 1 MΩ 105 22 kΩ 223 4.7 MΩ 475 $390~\Omega~....~391$ $6.8 \Omega 068$

ABBREVIATION AND MARKS

| ONE |
|------------|
| NG |
| |
| RMER |
| TOR |
| |
| NG RMER |

NOTE ON SAFETY:

Symbol A Fire or electrical shock hazard. Only original parts should be used to replaced any part marked with symbol A . Any other component substitution (other than original type), may increase risk of fire or electrical shock hazard.

安全上の注意:

▲ がついている部品は、安全上重要な部品です。必ず 指定されている部品番号の部品を使用して下さい。

| POS. | VERS. | PART NO. (FOR PCS) | DESCRIPTION | PART NO. (MJI) | POS. | VERS. COLOR | PART NO. (FOR PCS) | DESCRIPTION | PART NO. |
|--------------|---------|---|--|--------------------------|--------------|----------------|----------------------------------|--|--------------------------|
| NO | COLOR | (FUN FUS) | | (14101) | | 332311 | (1. 0111 00) | | (11101) |
| | | | P101-SERVO CIRCUIT BOARD | | R124 | | 4822 051 30102 | CHIP 1kΩ ±5% 1/16W | NN05102610 |
| | | | P101-CAPACITORS | | R125 | | 4822 117 12902 | CHIP 8.2kΩ ±5% 1/16W | NN05822610 |
| C101 | | 4822 126 11669 | CER. CHIP 27pF ±5% | DD95270300 | R126 | | | | |
| C102 | 331/340 | 4822 126 13883 | | DD95221300 | } | 330/331 | 4822 051 30332 | CHIP 3.3kΩ ±5% 1/16W | NN05332610 |
| C103 | | 4822 126 11669 | CER. CHIP 27pF ±5% | DD95270300 | R130 R131 | 330/331 | 4822 051 30103 | CHIP 10kΩ ±5% 1/16W | NN05103610 |
| C104 | | 4822 126 14417 4822 126 13837 | | DK96103300 DK96104200 | R133 | 330/331 | 4822 116 82487 | CHIP 0Ω ±5% 1/16W | NN05000610 |
| C105 C106 | | 4822 126 13837 | CER. CHIP 0.1µF ±10% B 10V | DK96104200 | R134 | 000,001 | 4822 051 30103 | CHIP 10kΩ ±5% 1/16W | NN05103610 |
| C107 | | 4822 122 31765 | CER. CHIP 100pF ±5% CG 50V | DD95101300 | R135 | | 4822 117 12902 | CHIP 8.2kΩ ±5% 1/16W | NN05822610 |
| C108 | | 4822 122 31765 | CER. CHIP 100pF ±5% CG50V | DD95101300 | R137 | 330/331 | 4822 116 82487 | CHIP 0Ω ±5% 1/16W | NN05000610 |
| C109 | | 4822 126 14417 | | DK96103300 | D004 | | 4000 054 00000 | CLUD COKO . EV. 4/46W | NN05683610 |
| C110 | | 4822 122 33761 | CER. CHIP 22pF ±5% CG 50V ELECT 100µF 10V | DD95220300 EJ10701010 | R201 R202 | | 4822 051 30683 4822 051 30683 | CHIP 68kΩ ±5% 1/16W CHIP 68kΩ ±5% 1/16W | NN05683610 |
| C111 C112 | | | ELECT 100µF 10V | EJ10701010 | R203 | | 4822 051 30272 | CHIP 2.7kΩ ±5% 1/16W | NN05272610 |
| C113 | | 4822 126 13837 | CER. CHIP 0.1µF ±10% B 10V | DK96104200 | R204 | | 4822 051 30184 | CHIP 180kΩ ±5% 1/16W | NN05184610 |
| C114 | | ,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,, | ELECT 47µF 10V | EJ47601010 | R205 | | 4822 051 30184 | CHIP 180kΩ ±5% 1/16W | NN05184610 |
| C115 | | 4822 126 13837 | CER. CHIP 0.1µF ±10% B 10V | DK96104200 | R206 | | 4822 051 30333 | CHIP 33kΩ ±5% 1/16W | NN05333610 |
| C116 | 1 | 4822 126 13837 | CER. CHIP 0.1µF ±10% B 10V | DK96104200 | R207 R208 | | 4822 117 12891 4822 117 12891 | CHIP $220k\Omega \pm 5\% 1/16W$ CHIP $220k\Omega \pm 5\% 1/16W$ | NN05224610 NN05224610 |
| C117 | 330/331 | 4822 126 13837 | CER. CHIP 0.1µF ±10% B 10V CER. CHIP 100pF ±5% CG 50V | DK96104200 DD95101300 | R208 | | 4822 117 12891 | CHIP 18kΩ ±5% 1/16W | NN05224610 |
| C118 C119 | 330 | 4822 122 31765 | CER. CHIP 100pF ±5% CG 50V | DD93101300 DD90020300 | R210 | 330/331 | 4822 116 82487 | CHIP 0Ω ±5% 1/16W | NN05000610 |
| C120 | 1000 | 4822 126 14417 | l ' | DK96103300 | R211 | 330/331 | 4822 116 82487 | CHIP 0Ω ±5% 1/16W | NN05000610 |
| | | | | | R212 | 1 | | CHIP 4.7kΩ ±5% 1/16W | NN05472610 |
| C201 | | 5322 126 11578 | CER. CHIP 1000pF ±10% B | DK96102300 | R213 | 330/331 | 4822 051 30102 | CHIP 1kΩ ±5% 1/16W | NN05102610 |
| C202 | | 5322 126 11578 | CER. CHIP 1000pF ±10% B | DK96102300 | R214 R215 | | 4822 051 30103 4822 051 30153 | CHIP 10kΩ ±5% 1/16W CHIP 15kΩ ±5% 1/16W | NN05103610 NN05153610 |
| C203 | | 4822 126 13837 | ELECT 100µF 10V CER. CHIP 0.1µF ±10% B 10V | EJ10701010 DK96104200 | R215 | | 4822 051 30153 | CHIP 12kΩ ±5% 1/16W | NN05153610 |
| C204 C205 | | 4822 126 13637 | CER. CHIP 4700P ±10% 50V | DK96472300 | R217 | 331/340 | 4822 051 30153 | CHIP 15kΩ ±5% 1/16W | NN05153610 |
| C206 | | 4822 126 11685 | CER. CHIP 4700P ±10% 50V | DK96472300 | R251 | 340 | | CHIP 82Ω ±5% 1/10W | NI05820110 |
| C207 | 1 | | ELECT 100µF 10V | EJ10701010 | R252 | 340 | | CHIP 82Ω ±5% 1/10W | NI05820110 |
| C208 | | 4822 126 13837 | CER. CHIP 0.1µF ±10% B 10V | DK96104200 | R253 | 340 | 4822 051 30682 | CHIP 6.8kΩ ±5% 1/16W | NN05682610 |
| C209 | | 5322 126 11578 | CER. CHIP 1000pF ±10% B | DK96102300 DK96102300 | R254 R255 | 340 340 | 4822 051 30682 4822 051 30682 | CHIP 6.8kΩ ±5% 1/16W CHIP 6.8kΩ ±5% 1/16W | NN05682610 NN05682610 |
| C210 | | 5322 126 11578 5322 126 11578 | CER. CHIP 1000pF ±10% B CER. CHIP 1000pF ±10% B | DK96102300 DK96102300 | R256 | 340 | 4822 117 13632 | CHIP 0.0KΩ ±5% 1/16W | NN05002010 |
| C211 | | 5322 126 11578 | CER. CHIP 1000pF ±10% B | DK96102300 | R257 | 340 | 4822 051 30103 | CHIP 10kΩ ±5% 1/16W | NN05103610 |
| C213 | | 4822 126 11568 | CER. CHIP 470pF ±10% | DK96471300 | R258 | 340 | 4822 051 30103 | CHIP 10kΩ ±5% 1/16W | NN05103610 |
| C214 | 1 | | CER. CHIP 0.22µF ±10% B 16V | DK56224200 | R259 | 340 | | CHIP 2.2Ω ±5% 1/10W | NI05022110 |
| C215 | | 4822 126 11568 | CER. CHIP 470pF ±10% | DK96471300 | R260 | 340 | | CHIP 2.2Ω ±5% 1/10W | NI05022110 NI05022110 |
| C216 | | 4822 126 14417 | CER. CHIP 0.01µF ±10% 50V K CER. CHIP 0.47µF ±10% 16V B | DK96103300 DK56474200 | R261 R262 | 340 340 | 4822 051 30223 | CHIP 2.2Ω ±5% 1/10W CHIP 22kΩ ±5% 1/16W | NN05223610 |
| C217 | 3 331 | 4822 126 13837 | CER. CHIP 0.1µF ±10% 10V B | DK96104200 | | 1 | 4822 051 30223 | CHIP 22kΩ ±5% 1/16W | NN05223610 |
| C25 | 1 | 4822 126 13837 | CER. CHIP 0.1µF ±10% B 10V | DK96104200 | R264 | 340 | 4822 051 30223 | CHIP 22kΩ ±5% 1/16W | NN05223610 |
| | 340 | 4822 126 13837 | CER. CHIP 0.1µF ±10% B 10V | DK96104200 | R265 | 340 | 4822 116 82487 | CHIP 0Ω ±5% 1/16W | NN05000610 |
| C250 | 1 | 4822 126 13837 | CER. CHIP 0.1µF ±10% B 10V | DK96104200 | R266 | 340 | | CHIP 2.2Ω ±5% 1/10W | NI05022110 |
| C254 | | 1000 100 10007 | ELECT 100µF 10V CER. CHIP 0.1µF ±10% B 10V | EJ10701010 DK96104200 | | | | P101-SEMICONDUCTORS | |
| C25 | | 4822 126 13837 4822 126 13837 | CER. CHIP 0.1µF ±10% B 10V | DK96104200 DK96104200 | Q101 | 330/331 | 9965 000 01600 | IC TZA1022 HF AMP/LA CONT | HC10180490 |
| C25 | | 4822 126 13837 | CER. CHIP 0.1µF ±10% B 10V | DK96104200 | Q102 | 000,001 | 9965 000 01720 | IC NJM2137M-TE1 DUAL OP | HC10206090 |
| C25 | | 4822 126 13396 | CER. CHIP 0.047µF ±10% 16V | DK96473200 | Q103 | | 4822 209 30455 | IC NJM2100M JRC | HC10085090 |
| | | | | | Q104 | | 4822 130 61906 | DIG.TRS. DTC114EU | BA20035210 |
| | | | P101-RESISTORS | NINIOECOTOTO | Q105 | | 4822 130 61906 | DIG.TRS. DTC114EU | BA20035210 |
| R10 | | 4822 051 30681 4822 051 30681 | CHIP 680Ω ±5% 1/16W CHIP 680Ω ±5% 1/16W | NN05681610 NN05681610 | Q106 Q107 | | 4822 130 60731 4822 130 61906 | CHIP TRS. 2SA1036K Q R DIG.TRS. DTC114EU | HX110362B0 BA20035210 |
| R10: R10: | | 4822 051 30681 | CHIP 680Ω ±5% 1/16W CHIP 22kΩ ±5% 1/16W | NN05223610 | Q107 Q108 | | 4822 130 11357 | DIG.TRS. RN2307 DTA114YU | BA12307000 |
| R10 | | 4822 051 30223 | CHIP 22kΩ ±5% 1/16W | NN05223610 | Q109 | | 9965 000 01601 | THERMISTOR | HH50005780 |
| R10 | | 4822 117 12925 | CHIP 47kΩ ±5% 1/16W | NN05473610 | | | | TN10-4C103JT 10k | |
| R10 | 6 | 4822 051 30153 | 1 | NN05153610 | | | 4822 130 61906 | DIG.TRS. DTC114EU | BA20035210 |
| R10 | 1 | 4822 051 30102 | l . | NN05102610 NN05152610 | Q112 | 340 | 9965 000 01601 | THERMISTOR TN10-4C103JT 10k | HH50005780 |
| R10 R10 | | 4822 051 30152 4822 051 30474 | CHIP 1.5kΩ ±5% 1/16W CHIP 470kΩ ±5% 1/16W | NN05152610 NN05474610 | Q201 | | 4822 209 16372 | IC TDA7073AT | HC10165490 |
| R10 | | 4822 051 30474 | | NN05103610 | 4201 | | | SOP DUAL BTL DRIVER | |
| R11 | | 4822 051 30333 | 1 | NN05333610 | Q202 | | 4822 209 16372 | IC TDA7073AT | HC10165490 |
| R11 | | 4822 051 30333 | CHIP 33kΩ ±5% 1/16W | NN05333610 | | | | SOP DUAL BTL DRIVER | |
| R11 | | 4822 051 30562 | | NN05562610 | Q203 | 331/340 | 9965 000 01601 | THERMISTOR | HH50005780 |
| R11 | | 4822 116 82487 | CHIP 0Ω ±5% 1/16W | NN05000610 | 0054 | 340 | 4922 200 16277 | TN10-4C103JT 10k | HC10012010 |
| R11 | | 4822 117 12891 4822 051 30102 | CHIP 220k Ω ±5% 1/16W CHIP 1k Ω ±5% 1/16W | NN05224610 NN05102610 | Q251 | 340 | 4822 209 16877 | IC BA6856FP 3PH-MOTOR DRIVER | HC10213210 |
| R11 | i i | 4822 051 30102 | | NN05102610 | Q252 | 340 | 4822 130 60731 | CHIP TRS. 2SA1036K Q R | HX110362B0 |
| R12 | 1 | 4822 117 13632 | CHIP 100kΩ ±5% 1/16W | NN05104610 | | | | | |
| | | 4822 117 13632 | CHIP 100kΩ ±5% 1/16W | NN05104610 | | 1 | 1 | i e | |

| | VERS. | PART NO. (FOR PCS) | DESCRIPTION | PART NO. (MJI) | POS. | VERS. COLOR | PART NO. (FOR PCS) | DESCRIPTION | PART NO. (MJI) |
|----------------|--------------------|----------------------------------|--|--------------------------|--------------|----------------|--------------------------------------|--|--------------------------|
| NO (| JOLON | (1 O.1.1 OO) | | () | | | , ==/ | | |
| | | | P101-MISCELLANEOUS | | CD10 | | 9965 000 00599 | CER. CHIP 0.22µF ±10% B 10V | DK96224200 |
| J101 | | | JACK 16FMZ-ST FFC CONN. | YJ07020820 | CD11 | | 9965 000 00599 | CER. CHIP 0.22µF ±10% B 10V | |
| J102 | - | | JACK 20FE-ST-VK-N 20PIN | YJ07020160 | CD12 | | 4822 126 13837 | CER. CHIP 0.1µF ±10% B 10V | DK96104200 |
| J202 | | | JACK 11FM-1.0ST FFC CONN. | YJ07020830 | CD13 | | 4822 122 32672 | TANTL.CHIP 1µF 16V | EY10501610 OA47801020 |
| | | | Deed TRANS SIRSI III TO A TO | | CD14 | | 5322 124 41744 | ELECT. 4700µF 10V RA-2 CER. CHIP 18pF ±5% | DD95180300 |
| | | | P801-TRANS CIRCUIT BOARD | TS15747010 | CD15 CD16 | | 4822 126 13689 4822 122 33752 | CER. CHIP 15pF ±5% CG 50V | DD95160300 DD95150300 |
| ▲ L801 | /F, /U | | MAINS TRANSF. EI-57 100V/120V | 1313/4/010 | CD16 | | 4822 126 13837 | CER. CHIP 0.1µF ±10% B 10V | DK96104200 |
| ▲ L801 | /N | 9965 000 01599 | MAINS TRANSF. EI-57 230V | TS15747020 | CD18 | | 4822 126 13396 | | |
| LOVI | (1) | 2300 300 0 1003 | | | CD19 | 1 | 4822 126 11671 | CER. CHIP 33pF ±5% CG 50V | DD95330300 |
| | | | P851-POWER SW | | | | 5322 126 14449 | CER. CHIP 39pF ±5% CG 50V | DD95390300 |
| | | | CIRCUIT BOARD | DI(174.000.10 | CD20 | 1 | 4822 122 33753 | CER. CHIP 150pF ±5% CG 50V | DD95151300 DD95180300 |
| ▲ C851 | | 4822 122 33276 | CER. DE7150 F 103M VA1 KC | DK17103840 FN01020020 | CD21 | | 4822 126 13689 5322 126 11578 | CER. CHIP 18pF ±5% CG 50V CER. CHIP 1000pF ±10% B | DK96102300 |
| ▲ L851 | | 4822 157 70419 | LF-4D-102 PUSH SWITCH SDDLD1 | SP01011990 | | | 4822 122 31765 | CER. CHIP 1000PF ±5% CG 50V | |
| ▲ \$851 | | 4822 276 13364 | POWER TV-3 | 51 01011000 | 1 | 33,7040 | | | |
| 1 | | | 311211110 | | CM01 | | 5322 122 32654 | CER. CHIP 0.022µF ±10% 16V | DK96223200 |
| | | | PB01-XLR CONN. CIRCUIT | | CT01 | | 4822 126 13837 | CER. CHIP 0.1µF ±10% B 10V | DK96104200 |
| | | | BOARD [PMD331/340] | | CT03 | 1 | 4822 126 12339 | CER. CHIP 2200P ±10% 50V | DK96222300 |
| | | | PB01-CAPACITORS | 0400700000 | CT04 | | 4822 126 13837 | CER. CHIP 0.1µF ±10% B 10V | DK96104200 DK96104200 |
| | | 4822 124 41537 | ELECT. 220µF M 6.3V RA-2 | OA22700620 DD38104010 | CT07 | 1 | 4822 126 13837 4822 124 22275 | CER. CHIP 0.1µF ±10% B 10V ELECT. 47µF M 10V RA-2 | OA47601020 |
| CT53 | 331/340 | | CER. 50V DC 0.1µF +80 -20% CER. 0.01µF ±10% 50V | DK16103300 | 1 0100 | 001/040 | 1022 127 22213 | LELOT. TI μι WI TOV LIA-Z | 5, 551020 |
| CT56 CT57 | 331/340 331/340 | | CER. 0.01µF ±10% 50V | DK16103300 | CU01 | | 4822 122 33752 | CER. CHIP 15pF ±5% CG 50V | DD95150300 |
| | 331/340 | | CER. 220pF ±10% 50V | DK16221300 | CU02 | | 4822 122 33752 | CER. CHIP 15pF ±5% CG 50V | DD95150300 |
| CT59 | 331/340 | 1 | CER. 220pF ±10% 50V | DK16221300 | CU03 | | 4822 126 13837 | CER. CHIP 0.1µF ±10% B 10V | DK96104200 |
| | | | | | CU04 | 4 | 4822 126 13837 | CER. CHIP 0.1µF ±10% B 10V | DK96104200 OA10505020 |
| | 05:/=:- | | PB01-RESISTORS | GG05560160 | CU05 CU41 | | 4822 124 41543 4822 126 13837 | ELECT. 1µF M 50V RA-2 CER. CHIP 0.1µF ±10% B 10V | DK96104200 |
| RT52 | 331/340 | | 56Ω ±5% 1/6W 56Ω ±5% 1/6W | GG05560160 GG05560160 | CU51 | | 4822 124 90352 | ELECT. 10µF M 16V RA-2 | OA10601620 |
| H153 | 331/340 | 1 | J022 TO /0 1/044 | 3333333100 | CU53 | | 4822 126 13837 | CER. CHIP 0.1µF ±10% B 10V | DK96104200 |
| | | | PB01-SEMICONDUCTOR | | CU54 | | 5322 126 11578 | CER. CHIP 1000pF ±10% B | DK96102300 |
| QT52 | 331/340 | 5322 209 60473 | IC SN75158/P TEXAS INST. | HC10071370 | Cñec | | | | |
| | | | | |) | | 5322 126 11578 | CER. CHIP 1000pF ±10% B | DK96102300 |
| 1 | 1 | | PB01-MISCELLANEOUS | V 104004070 | CU79 | | 4822 126 13837 | CER. CHIP 0.1µF ±10% B 10V | DK96104200 |
| JB53 | | 1 | JACK NC3MAH 3P CANNON JACK NC3MAH 3P CANNON | YJ01004070 YJ01004070 | CU80 | 1 | 4822 126 13837 | CER. CHIP 0.1µF ±10% B 10V | DK96104200 |
| JB54 JT53 | | | JACK NC3MAH 3P CANNON | YJ01004070 | 3031 | | 1.522 123 10007 | | |
| 3153 | 001/040 | | | | C301 | | 4822 124 90352 | ELECT. 10µF M 16V RA-2 | OA10601620 |
| LT52 | 331/340 | 4822 242 73843 | EMI FILTER DSS306-91-F-223Z | | C302 | | 4822 126 13837 | CER. CHIP 0.1µF ±10% B 10V | DK96104200 |
| LT53 | 331/340 | 4822 148 81381 | PULSE TRANSF. TC-1086-26 | TP33842010 | | | 4822 126 13837 | CER. CHIP 0.1µF ±10% B 10V | DK96104200 DK96104200 |
| LT54 | 331/340 |) | FERRITE CORE | FC90050040 | C304 C305 | | 0 4822 126 13837 0 4822 126 13396 | CER. CHIP 0.1µF ±10% B 10V CER. CHIP 0.047µF ±10% X7R | |
| 1755 | 201/040 | | BL02RN1-R62T2 FERRITE CORE | FC90050040 | 0305 | 001/04 | 7022 120 13390 | ΟΕΙ Ι. ΟΙ ΙΙΙ΄ Ο.04/μΕ ΞΙΟ /0 Χ/Π | 51,00770200 |
| LT55 | 331/340 | Ί | BL02RN1-R62T2 | , 55555555 | C501 | | 4822 124 90352 | ELECT. 10µF M 16V RA-2 | OA10601620 |
| 1 | | | | | C502 | : | 4822 124 90352 | ELECT. 10µF M 16V RA-2 | OA10601620 |
| | | | PD01-MAIN CIRCUIT BOARD | | C503 | | 4822 122 33761 | CER. CHIP 22pF ±5% CG 50V | DD9522030 |
| | | | PP04 04540#055 | | C504 | 1 | 4822 122 33761 | CER. CHIP 22pF ±5% CG 50V | DD9522030 |
| | | 4000 404 00054 | PD01-CAPACITORS | OA10701620 | C505 | 1 | 4822 122 33753 4822 122 33753 | CER. CHIP 150pF ±5% 50V CER. CHIP 150pF ±5% 50V | DD9515130 |
| CB0 | | 4822 124 90354 | ELECT. 100µF M 16V RA-2 | OA10/01020 | C507 | | 4822 122 33753 | CER. CHIP 150pF ±5% 50V | DD9515130 |
| CB02 | | 4822 124 90352 | ELECT. 10µF M 16V RA-2 | OA10601620 | C508 | | 4822 122 33753 | CER. CHIP 150pF ±5% 50V | DD9515130 |
| CB0 | 1 | 12- 00002 | | | C509 | | 4822 124 90354 | ELECT. 100µF M 16V RA-2 | OA1070162 |
| CB0 | | | | | C510 |) | 4822 124 90354 | ELECT. 100µF M 16V RA-2 | OA1070162 |
| 5 | | 4822 124 90354 | ELECT. 100µF M 16V RA-2 | OA10701620 | C511 | | 5322 126 11578 | CER. CHIP 1000pF ±10% B | DK9610230 |
| CB1 | | | | | C512 | | 5322 126 11578 | CER. CHIP 1000pF ±10% B | DK9610230 |
| CB1 | 4 | 14000 100 00701 | CED CHIP 225E 1EW CO EOV | DD95220300 | C513 | | 4822 126 13837 4822 126 13837 | CER. CHIP 0.1µF ±10% B 10V CER. CHIP 0.1µF ±10% B 10V | DK96104200 |
| \ \{CP2 | | 0 4822 122 33761 | CER. CHIP 22pF ±5% CG 50V | DD33220300 | C515 | | 4822 126 13837 | CER. CHIP 0.1µF ±10% B 10V | DK96104200 |
| CB2 | 1 | | | | C516 | | 4822 124 90352 | ELECT. 10µF M 16V RA-2 | OA1060162 |
| CDO | 1 330/33 | 1 4822 126 11685 | CER. CHIP 4700pF ±10% B 50\ | DK96472300 | C517 | | 4822 126 13837 | CER. CHIP 0.1µF ±10% B 10V | DK96104200 |
| CDO | | 4822 126 | CER. CHIP 3300pF ±10% B 50\ | DK96332300 | C518 | 3 | | | |
| CDO |)2 | 4822 126 13396 | The state of the s | DK96473200 |) S | | 4822 124 90352 | ELECT. 10µF M 16V RA-2 | OA1060162 |
| CDO | | 5322 126 11578 | | DK96102300 | C522 | | 4000 104 00054 | FLECT 1000FM16V BA 2 | OA1070162 |
| CDO | | 5322 126 11578 | | DK96102300 DK96103300 | C523 | 1 | 4822 124 90354 4822 124 90354 | ELECT. 100µF M 16V RA-2 ELECT. 100µF M 16V RA-2 | OA1070162 |
| | J5 | 4822 126 14417 | CER. CHIP 0.47µF ±10% 50V K | | C525 | 1 | 4822 126 13837 | CER. CHIP 0.1µF ±10% B 10V | DK96104200 |
| CDC | ne l | | | | | 1 | 1 | | |
| CDC | | 4822 126 13396 | | DK96473200 | C526 | 6 | 4822 126 13837 | CER. CHIP 0.1µF ±10% B 10V | DK96104200 |
| | 7 | 4822 126 13396 4822 126 12495 | CER. CHIP 0.047µF ±10% X7R | | | | 4822 126 13837 0 4822 126 13837 | CER. CHIP 0.1µF ±10% B 10V CER. CHIP 0.1µF ±10% B 10V | 1 |

| | | | <u> </u> | | | | | 1 | |
|---------------------|---------|----------------------------------|--|--------------------------|--------------------------|---------|----------------------------------|--|--------------------------|
| POS. | VERS. | PART NO. | DESCRIPTION | PART NO. | POS. | VERS. | PART NO. | DESCRIPTION | PART NO. |
| NO | COLOR | (FOR PCS) | DESCRIPTION | (MJI) | NO | COLOR | (FOR PCS) | DEGOTHI HON | (MJI) |
| | | | | | | | | | |
| 0004 | 000 | 4822 124 22243 | ELECT 6800µF 16V RE3 | OA68801620 | RT03 | | 4822 051 30331 | CHIP 330Ω ±5% 1/16W | NN05331610 |
| | 330 | 4822 124 22243 | · · · · · · · · · · · · · · · · · · · | EA10901670 | RT04 | | 4822 051 30759 | CHIP 75Ω ±5% 1/16W | NN05750610 |
| | 331/340 | | ELECT 10000µF 16V RE3 | DD38104010 | n104 | | 4022 051 50759 | CHIF 7522 ±5% 1/16W | 141405750610 |
| C802 | | | CER. 0.1µF +80%-20% 50V | | Duna | | 4000 054 00400 | 01110 401-0 50/ 4/4014 | NINIOTA 00040 |
| C803 | | | CER. 0.1µF +80%-20% 50V | DD38104010 | RU01 | | 4822 051 30103 | CHIP 10kΩ ±5% 1/16W | NN05103610 |
| C804 | | 4822 124 90371 | ELECT. 470µF M 10V RA-2 | OA47701020 | RU02 | | 4822 051 30103 | CHIP 10kΩ ±5% 1/16W | NN05103610 |
| C805 | | | CER. 0.1µF +80%-20% 50V | DD38104010 | RU04 | | 4822 051 30474 | CHIP 470kΩ ±5% 1/16W | NN05474610 |
| C806 | | | CER. 0.1µF +80%-20% 50V | DD38104010 | RU05 | 330/331 | 4822 116 82487 | CHIP 0Ω ±5% 1/16W | NN05000610 |
| C807 | | 4822 124 90371 | ELECT. 470µF M 10V RA-2 | OA47701020 | RU06 | | 4822 051 30474 | CHIP 470kΩ ±5% 1/16W | NN05474610 |
| C808 | | 4822 124 11583 | ELECT. 2200µF M 35V RA-2 | OA22803520 | RU07 | | 4822 051 30474 | CHIP 470kΩ ±5% 1/16W | NN05474610 |
| C809 | | 4822 124 11583 | ELECT. 2200µF M 35V RA-2 | OA22803520 | RU08 | | 4822 051 30474 | CHIP 470kΩ ±5% 1/16W | NN05474610 |
| C810 | | 1022 121 11000 | CER. 0.1µF +80%-20% 50V | DD38104010 | RU09 | | 4822 051 30103 | CHIP 10kΩ ±5% 1/16W | NN05103610 |
| C811 | 1 | | CER. 0.1µF +80%-20% 50V | DD38104010 | RU10 | 330/340 | 1 | CHIP 0Ω ±5% 1/16W | NN05000610 |
| | | | CER. 0.1µF +80%-20% 50V | DD38104010 | RU11 | 000/0/0 | 1022 110 02 101 | 01111 022 2070 171011 | 111100000010 |
| C812 | | | CER. 0.1µF +80%-20% 50V | DD38104010 | 1 11011 | | 4822 051 30103 | CHIP 10kΩ ±5% 1/16W | NN05103610 |
| C813 | | | | 1 1 | RU21 | 1 | 4022 031 30103 | CHIP 10K22 ±5% 1/16VV | 141405103610 |
| C814 | 1 | 4822 124 22277 | ELECT. 470µF 16V M RA-2 | OA47701620 | | | 1000 051 00171 | 01110 4701 0 50/ 4/4014/ | 11105474040 |
| C815 | | 4822 124 22277 | ELECT. 470µF 16V M RA-2 | OA47701620 | RU22 | | 4822 051 30474 | CHIP 470kΩ ±5% 1/16W | NN05474610 |
| C816 | | 4822 124 41543 | ELECT. 1µF M 50V RA-2 | OA10505020 | RU23 | | | | |
| | 1 | | 200 240 - 240 | | } | | 4822 051 30103 | CHIP 10kΩ ±5% 1/16W | NN05103610 |
| | | | PD01-RESISTORS | | RU27 | | | | |
| RB01 | | 9965 000 01716 | VARIABLE 10kΩ B | RK01031580 | RU28 | | 4822 051 30474 | CHIP 470kΩ ±5% 1/16W | NN05474610 |
| RB02 | 331/340 | 9965 000 01716 | VARIABLE 10kΩ B | RK01031580 | RU30 | 1 | 4822 051 30105 | CHIP 1MΩ ±5% 1/16W | NN05105610 |
| RB03 | | 4822 051 30221 | CHIP 220Ω ±5% 1/16W | NN05221610 | RU31 | 1 | 4822 051 30474 | CHIP 470kΩ ±5% 1/16W | NN05474610 |
| RB04 | 1 | 4822 051 30221 | CHIP 220Ω ±5% 1/16W | NN05221610 | RU32 | 330 | 4822 051 30103 | CHIP 10kΩ ±5% 1/16W | NN05103610 |
| RB05 | | 4822 051 30103 | CHIP 10kΩ ±5% 1/16W | NN05103610 | RU41 | | 4822 051 30472 | CHIP 4.7kΩ ±5% 1/16W | NN05472610 |
| RB06 | 1 | 4822 051 30103 | CHIP 10kΩ ±5% 1/16W | NN05103610 | RU42 | | 4822 051 30472 | CHIP 4.7kΩ ±5% 1/16W | NN05472610 |
| RB07 | | 4822 051 30682 | CHIP 6.8kΩ ±5% 1/16W | NN05682610 | RU51 | | 4822 116 82487 | CHIP 0Ω ±5% 1/16W | NN05000610 |
| | | 1 | CHIP 6.8kΩ ±5% 1/16W | NN05682610 | RU52 | | 4822 117 12925 | CHIP 47kΩ ±5% 1/16W | NN05473610 |
| RB08 | | 4822 051 30682 | | | 1 | | 4822 116 83819 | | 1 |
| RB09 | | 4822 051 30223 | CHIP 22kΩ ±5% 1/16W | NN05223610 | RU53 | | | CHIP 18kΩ ±5% 1/16W | NN05183610 |
| RB10 | 331/340 | 4822 051 30223 | CHIP 22kΩ ±5% 1/16W | NN05223610 | RU54 | | 4822 051 30479 | CHIP 47Ω ±5% 1/16W | NN05470610 |
| RB11 | 1 | 4822 051 30223 | CHIP 22kΩ ±5% 1/16W | NN05223610 | RU55 | 3 | 4822 051 30102 | CHIP 1kΩ ±5% 1/16W | NN05102610 |
| RB12 | 331/340 | 4822 051 30223 | CHIP 22kΩ ±5% 1/16W | NN05223610 | RU58 | 1 | 4822 051 30103 | CHIP 10kΩ ±5% 1/16W | NN05103610 |
| RB13 | | | | 1 | RU60 | 331/340 | 4822 051 30102 | CHIP 1kΩ ±5% 1/16W | NN05102610 |
| 5 | 331/340 | 4822 051 30472 | CHIP 4.7kΩ ±5% 1/16W | NN05472610 | RU62 | | | | |
| RB24 | | | | | } | 331/340 | 4822 051 30102 | CHIP 1kΩ ±5% 1/16W | NN05102610 |
| RB25 | | | | | RU74 | | | | |
| 5 | | 4822 051 30479 | CHIP 47Ω ±5% 1/16W | NN05470610 | RU80 | | | | İ |
| RB28 | | | | | 5 | 331/340 | 4822 117 12925 | CHIP 47kΩ ±5% 1/16W | NN05473610 |
| RB29 | | | | | RU90 | | | | |
| 1023 | | 4822 051 30472 | CHIP 4.7kΩ ±5% 1/16W | NN05472610 | RU91 | | 4822 051 30474 | CHIP 470kΩ ±5% 1/16W | NN05474610 |
| RB32 | | 14022 001 00 172 | | | RU92 | | 4822 051 30474 | CHIP 470kΩ ±5% 1/16W | NN05474610 |
| RB33 | | | | | | 331/340 | 4822 117 12925 | CHIP 47kΩ ±5% 1/16W | NN05473610 |
| | | 4822 051 30103 | CHIP 10kΩ ±5% 1/16W | NN05103610 | | | 4822 117 12925 | CHIP 47kΩ ±5% 1/16W | NN05473610 |
| } DD00 | | 14822 051 30103 | CHIF 10K22 ±5 % 1/10VV | 141403103010 | RU95 | 1 | 4822 117 12925 | CHIP 47kΩ ±5% 1/16W | NN05473610 |
| RB36 | | | | | HU95 | 331/340 | 4022 117 12925 | CHIP 47K22 ±5% 1/16W | 141405473610 |
| | | | 0.45 | | Door | 004/040 | 4000 054 00474 | 01110 4701 0 50/ 4/4014 | |
| RD01 | | 4822 051 30222 | CHIP 2.2kΩ ±5% 1/16W | NN05222610 | R301 | 331/340 | 4822 051 30474 | CHIP 470kΩ ±5% 1/16W | NN05474610 |
| RD02 | 2 | 4822 051 30153 | CHIP 15kΩ ±5% 1/16W | NN05153610 | R501 | | 4822 051 30103 | CHIP 10kΩ ±5% 1/16W | NN05103610 |
| RD03 | 3 | 4822 051 30153 | CHIP 15kΩ ±5% 1/16W | NN05153610 | R502 | | 4822 051 30103 | CHIP 10kΩ ±5% 1/16W | NN05103610 |
| RD04 | 1 | 4822 051 30474 | CHIP 470kΩ ±5% 1/16W | NN05474610 | R503 | | 4822 051 30472 | CHIP 4.7kΩ ±5% 1/16W | NN05472610 |
| RD05 | 5 | 4822 051 30103 | CHIP 10kΩ ±5% 1/16W | NN05103610 | R504 | | 4822 051 30472 | CHIP 4.7kΩ ±5% 1/16W | NN05472610 |
| RD06 | 1 | 4822 117 13632 | CHIP 100kΩ ±5% 1/16W | NN05104610 | R505 | | 4822 116 83819 | CHIP 18kΩ ±5% 1/16W | NN05183610 |
| RD07 | | 4822 051 30332 | CHIP 3.3kΩ ±5% 1/16W | NN05332610 | R506 | | 4822 116 83819 | CHIP 18kΩ ±5% 1/16W | NN05183610 |
| RDO | | 4822 051 30332 | CHIP 3.3kΩ ±5% 1/16W | NN05332610 | R507 | | 4822 051 30333 | CHIP 33kΩ ±5% 1/16W | NN05333610 |
| RDOS | | 4822 051 30103 | CHIP 10kΩ ±5% 1/16W | NN05103610 | R508 | | 4822 051 30333 | CHIP 33kΩ ±5% 1/16W | NN05333610 |
| RD10 | | 4822 051 30105 | CHIP 1MΩ ±5% 1/16W | NN05105610 | R509 | | 4822 051 30123 | CHIP 12kΩ ±5% 1/16W | NN05123610 |
| 1 | - 1 | 4822 051 30103 | CHIP 22kΩ ±5% 1/16W | NN05223610 | R510 | ŀ | 4822 051 30123 | CHIP 12kΩ ±5% 1/16W | NN05123610 |
| RD11 | | | | NN05223610 NN05473610 | R510 | | 4822 051 30123 | | NN05123610 |
| RD12 | | 4822 117 12925 | CHIP 47kΩ ±5% 1/16W | | | | 1 | CHIP 10kΩ ±5% 1/16W | |
| RD13 | 1 | 4822 051 30333 | CHIP 33kΩ ±5% 1/16W | NN05333610 | R512 | 1 | 4822 051 30103 | CHIP 10kΩ ±5% 1/16W | NN05103610 |
| RD15 | | 4822 051 30474 | CHIP 470kΩ ±5% 1/16W | NN05474610 | R513 | | 4822 051 30223 | CHIP 22kΩ ±5% 1/16W | NN05223610 |
| RD16 | 3 | 4822 051 30103 | CHIP 10kΩ ±5% 1/16W | NN05103610 | R514 | | 4822 051 30223 | CHIP 22kΩ ±5% 1/16W | NN05223610 |
| RD17 | 7 | 4822 051 30103 | CHIP 10kΩ ±5% 1/16W | NN05103610 | R515 | | 4822 051 30151 | CHIP 150Ω ±5% 1/16W | NN05151610 |
| RD18 | 3 | 4822 051 30103 | CHIP 10kΩ ±5% 1/16W | NN05103610 | R516 | | 4822 051 30151 | CHIP 150Ω ±5% 1/16W | NN05151610 |
| RD19 | 9 | 4822 051 30103 | CHIP 10kΩ ±5% 1/16W | NN05103610 | R517 | I | 4822 117 12925 | CHIP 47kΩ ±5% 1/16W | NN05473610 |
| RD20 | | 4822 051 30102 | CHIP 1kΩ ±5% 1/16W | NN05102610 | R518 | | 4822 117 12925 | CHIP 47kΩ ±5% 1/16W | NN05473610 |
| RD2 | | 4822 116 82487 | CHIP 0Ω ±5% 1/16W | NN05000610 | R519 | | 4822 051 30471 | CHIP 470Ω ±5% 1/16W | NN05471610 |
| | 3 331 | 4822 116 83819 | CHIP 18kΩ ±5% 1/16W | NN05183610 | R520 | | 4822 051 30471 | CHIP 470Ω ±5% 1/16W | NN05471610 |
| RD2 | | 4822 051 30153 | CHIP 15kΩ ±5% 1/16W | NN05153610 | R531 | 330 | 4822 116 82487 | CHIP 0Ω ±5% 1/16W | NN05000610 |
| HD2 | 340 | 1022 001 00100 | OTH TORSE TO /0 1/ TOWY | 141400100010 | R532 | 330 | 4822 116 82487 | CHIP 0Ω ±5% 1/16W | NN05000610 |
| | 11 | 4000 444 00007 | EUCE 470 14/4W | NEOE047440 | R533 | 330 | | | |
| A | | 1/18/2/2 1 1 1 UNUK / | FUSE 4.7Ω J 1/4W | NF05047140 | กวงง | JUU | 4822 116 82487 | CHIP 0Ω ±5% 1/16W | NN05000610 |
| ▲ RM0 | 1 | 4822 111 90967 | | NINIOECO4040 | DC44 | 224/040 | 1000 117 10000 | CUID 400kO - FO/ 4/40kB | NINIOTACAGAG |
| ARMO RMO RTO2 | 2 | 4822 051 30681 4822 051 30221 | CHIP $680\Omega \pm 5\% \ 1/16W$ CHIP $220\Omega \pm 5\% \ 1/16W$ | NN05681610 NN05221610 | R541 R542 | | 4822 117 13632 4822 051 30222 | CHIP 100kΩ ±5% 1/16W CHIP 2.2kΩ ±5% 1/16W | NN05104610 NN05222610 |

| | VERS. COLOR | PART NO. (FOR PCS) | DESCRIPTION | PART NO. (MJI) | POS. NO | VERS. COLOR | PART NO. (FOR PCS) | DESCRIPTION | PART NO. (MJI) |
|----------------------|----------------|--------------------------------------|---|--------------------------|-------------------|----------------|--|--|--------------------------|
| R543 | 331/340 | 4822 051 30222 | CHIP 2.2kΩ ±5% 1/16W | NN05222610 | | | | PD01-MISCELLANEOUS | |
| | | | CHIP 1kΩ ±5% 1/16W | NN05102610 | JD01 | | | JACK 20FE-BT-VK-N 20PIN | YJ07020530 |
| | | | | | JT01 | | 4822 267 31729 | TERMINAL 14X14 RA 1L1P BLK | |
| R801 | | | | | JT02 | 331/340 | 4822 267 31369 | OPT. CONN.GP1F32T | YJ15000090 |
| 5 | | 4822 051 30472 | CHIP 4.7kΩ ±5% 1/16W | NN05472610 | 11104 | | | OPTICAL OUTPUT JACK 30PIN 30FMN-BMTTR-TB | V 107016500 |
| R805 | | | | 1 | JU01 JU51 | | | TERMINAL RCA JACK ORG | YT02021090 |
| | | | PD01-SEMICONDUCTORS | | JU53 | 331/340 | 4822 265 11401 | PLUG D-SUB 25P FEMALE | YP11000180 |
| DD01 | 221/240 | 4822 130 83715 | ICHIP DIODE 1SS301 DAN202U | HZ21005000 | J501 | 001/040 | 4822 267 31727 | TERMINAL 14X14 RCA 2L2P | YT02021210 |
| | | 4822 130 83715 | CHIP DIODE 188301 DAN202U | HZ21005000 | | 1 | | | |
| DM02 | 001/040 | 9965 000 01598 | CHIP DIODE | HZ30821000 | LB01 | 331/340 | 9965 000 01343 | RELAY RELAY MR62-12SR | LY20120510 |
| 5.11.02 | | | UDZS8.2B MA8082-M | | LB02 | 331/340 | 9965 000 01343 | RELAY RELAY MR62-12SR | LY20120510 |
| DU01 | | 9965 000 01491 | CHIP DIODE DA227 | HZ20032210 | LD01 | | | CHIP INDUCTANCE | LU04472010 |
| | | | ARRAY 2PIECES-2125 | | 1.500 | 1 | | 4.7µH ±10% 1608 TYPE CHIP INDUCTANCE | LU04472010 |
| DU02 | | 9965 000 01491 | CHIP DIODE DA227 | HZ20032210 | LD02 | | | 4.7µH ±10% 1608 TYPE | L004472010 |
| | | 4000 400 00745 | ARRAY 2PIECES-2125 CHIP DIODE 1SS301 DAN202U | HZ21005000 | LD03 | | | CHIP INDUCTANCE | LU04471010 |
| DU03 | | 4822 130 83715 4822 130 83715 | CHIP DIODE 1SS301 DAN202U | HZ21005000 | LDOO | | | 0.47µH ±10% MLF1608 | |
| DU51 DU52 | | 4822 130 83715 | CHIP DIODE 1SS301 DAN202U | | LD04 | | | CHIP INDUCTANCE | LU04471010 |
| D501 | | 4822 130 83715 | CHIP DIODE 1SS301 DAN202U | | | | | 0.47µH ±10% MLF1608 | |
| D502 | | 4822 130 81324 | CHIP DIODE 1SS302 | HZ20018050 | LT01 | İ | 4822 142 60388 | PULSE TRANSF. FOR CD | TP41042010 |
| ▲ D801 | | 4822 130 83067 | DIODE D3SB 20 | HE20020290 | LT02 | 1 | | FERRIT BEADS | FC90020120 |
| | | | V=200V IO=3.0A | | | | | BK1608HM102-T | F000000100 |
| ▲ D802 | | 4822 130 10413 | DIODE BRIDGE D2SBA20 | HE20027290 | LT03 | 331/340 | 9 | FERRIT BEADS | FC90020120 |
| D803 | | 4822 130 83715 | CHIP DIODE 1SS301 DAN202L | HZ21005000 | L501 | | 9965 000 01343 | BK1608HM102-T RELAY MR62-12SR | LY20120510 |
| | | | | | Loui | | 9900 000 01343 | NELAT WINUZ-123N | L120120010 |
| QB01 | 004/040 | 4822 209 83357 | IC NJM4560M JRC | HC10029090 | SU51 | | 4822 277 21789 | SLIDE SWITCH SSSUI-6MM | SS02020970 |
| QB04 | | 4622 209 65557 | 10 1401V143001VI 31 10 | 11010025555 | XD01 | | 4822 242 10883 | CRYSTAL CM309S | JX16002360 |
| QD04 | | 9965 000 01437 | IC CXD2585Q CD DECODER | HC10069250 | | | | 16.9344MHz CITIZEN | |
| | | 9965 000 01601 | THERMISTOR | HH50005780 | XU01 | | 9965 000 01597 | CRYSTAL CM309S 20MHz | JX20001360 |
| 4502 | | | TN10-4C103JT 10k | | | | | | |
| QM01 | 1 | 4822 209 30193 | IC LB1641 MOTOR DRIVER | HC10279030 | | | | PV01-HEAD PHONE | |
| QU01 | 1 | 9965 000 01492 | MICROPROCESSOR | HU371KH00F | | | | CIRCUIT BOARD | |
| | | | HD643306ZF MPU | HC10074990 | CV01 | | | PV01-CAPACITORS ELECT 100µF 16V RC-2 | EJ10701610 |
| QU03 | 3 | 9965 000 01595 | IC AT25640 64K EEPROM | HC10074990 | CV02 | | | ELECT 100µF 16V RC-2 | EJ10701610 |
| 01107 | . | 9965 000 01596 | IC 74HC4094BT FLAT | HC809449R0 | CV0 | | | ELECT 47µF 16V | EJ47601610 |
| QU04 QU41 | | 4822 130 60731 | CHIP TRS. 2SA1036K Q R | HX110362B0 | CV04 | | | ELECT 47µF 16V | EJ47601610 |
| QU42 | | 4822 130 61906 | DIG.TRS. DTC114EU | BA20035210 | | | | | |
| QU51 | | 4822 130 60669 | CHIP TRS. 2SC4081 Q R | HX300012A0 | | | | PV01-RESISTORS | |
| | | | 2SC4116 Y GR | | RV0 | | 4822 051 30153 | CHIP 15kΩ ±5% 1/16W | NN05153610 NN05153610 |
| QU52 | | 4822 130 61906 | DIG.TRS. DTC114EU | BA20035210 | RV0 | | 4822 051 30153 | CHIP 15kΩ ±5% 1/16W | NN05153610 |
| QU53 | 1 | 4822 130 11357 | DIG.TRS. RN2307 DTA114YU | BA12307000 | RV0 | | 4822 051 30103 4822 051 30103 | CHIP $10k\Omega \pm 5\% 1/16W$ CHIP $10k\Omega \pm 5\% 1/16W$ | NN05103610 |
| QU55 | | 4822 130 11357 | DIG.TRS. RN2307 DTA114YU | BA12307000 HC809449R0 | RV0 | | 9965 000 01489 | CHIP 120Ω ±5% 1/2W | RI05121120 |
| QU56 | 1 | 9965 000 01596 | IC 74HC4094BT FLAT IC 74HC165F | HC716500R0 | RV0 | | 9965 000 01489 | CHIP 120Ω ±5% 1/2W | RI05121120 |
| QU57 | | 4822 209 17428 4822 209 17428 | | HC716500R0 | RV0 | 1 | 4822 051 30472 | CHIP 4.7kΩ ±5% 1/16W | NN05472610 |
| QU58 QU59 | 1 | 7022 203 17420 | 13 7 31 10 1001 | | RV0 | 1 | 4822 051 30472 | CHIP 4.7kΩ ±5% 1/16W | NN05472610 |
| \ \ \ \ \ \ \ \ | <u> </u> | 4822 130 61906 | DIG.TRS. DTC114EU | BA20035210 | RV0 | | 9965 000 01490 | VARIABLE 50kΩ A L=15MM | RM05033070 |
| QU64 | 4 | | | | 11 | | | | |
| QU9 | | 4822 209 30426 | IC CMOS 74HC00 FLAT | HC700000Z0 | | | | PV01-SEMICONDUCTOR | 110400 4500 |
| | | | | | QV0 | 1 | 4822 209 31378 | IC NJM4556AM JRC | HC10045090 |
| Q301 | | 0 9965 000 01717 | | HC10042770 | | | | DV01 MISCELL ANEOU | |
| Q302 | | 0 9965 000 01718 | | HC10089990 | 11 ,,,, | | 4000 007 01106 | JACK ST HEADPHONE BL/GL | YJ01003020 |
| Q501 | 1 | 4822 209 15226 | | HC10004610 | JV02 | ² | 4822 267 31126 | JACK ST HEADPHONE BUGL | 1301003020 |
| Q502 | | 4822 209 83357 | | HC10029090 BA12307000 | | | | PY01-DISPLAY | |
| Q541 | | 0 4822 130 11357 | | HT328782A0 | | | | CIRCUIT BOARD | |
| Q542 | | 0 4822 130 43818 0 4822 130 43818 | | HT328782A0 | 11 | | | PY01-CAPACITORS | |
| Q543 A Q80 | | 4822 209 83824 | | HC38905090 | CY0 | 1 | 9965 000 01438 | | EY22505020 |
| ▲ Q802 | | 4822 209 73674 | | HC38906090 | CY0 | | 9965 000 01438 | | EY22505020 |
| ▲ Q803 | | 4822 209 82829 | 1 | HC38515090 | CY0 | | 9965 000 01438 | · · | EY22505020 |
| ▲ Q804 | | 4822 209 83828 | | HC39515090 | CY0 | | 4822 124 23002 | | EY10601620 |
| | | 4822 130 61906 | DIG.TRS. DTC114EU | BA20035210 | CY0 | | 4822 126 14417 | | DK96103300 |
| Q80 | | 4822 130 61906 | DIG.TRS. DTC114EU | BA20035210 | CY0 | | 4822 124 11226 | TANTL.CHIP 22µF 6.3V | EY226O0620 |
| Q809 Q809 | 6 | 11022 100 01000 | | | | | | | |
| | 6 | 1022 100 01000 | | | CY0 | | 4822 126 14417 | | DK96103300 |
| | 6 | 1022 100 01000 | | | CY0 CY0 CY0 | 8 | 4822 126 14417 4822 126 14417 4822 126 14417 | CER. CHIP 0.01µF ±10% 50V k | DK96103300 |

| POS. NO | VERS. COLOR | PART NO. (FOR PCS) | DESCRIPTION | PART NO. (MJI) |
|--|----------------|---|---|--|
| CY10 | | 5322 126 11578 | CER. CHIP 1000pF ±10% B | DK96102300 |
| RY01 RY02 RY03 RY04 RY05 RY06 RY07 RY08 RY09 RY11 RY12 RY13 RY14 RY15 RY16 | | 9965 000 01444 4822 051 30332 4822 051 30103 4822 051 30103 4822 051 30103 4822 051 30103 4822 051 30101 4822 051 30101 4822 051 30101 4822 051 30121 4822 051 30121 4822 051 30121 4822 051 30121 4822 051 30101 | PY01-RESISTORS VARIABLE 100 $k\Omega$ B W/CLIC CHIP 3.3 $k\Omega$ ±5% 1/16W CHIP 10 $k\Omega$ ±5% 1/16W CHIP 27 $k\Omega$ ±5% 1/16W CHIP 10 $k\Omega$ ±5% 1/16W CHIP 10 $k\Omega$ ±5% 1/16W CHIP 10 $k\Omega$ ±5% 1/16W CHIP 100 Ω ±5% 1/16W CHIP 120 Ω ±5% 1/16W | RB01040080 NN05332610 NN05103610 NN05103610 NN05103610 NN05103610 NN05103610 NN05101610 NN05101610 NN05101610 NN05121610 NN05121610 NN05121610 NN05121610 NN05121610 |
| RY18 RY19 RY20 | | 4822 117 12891 4822 051 30121 4822 051 30221 | CHIP 220k Ω ±5% 1/16W CHIP 120 Ω ±5% 1/16W CHIP 220 Ω ±5% 1/16W | NN05224610 NN05121610 NN05221610 |
| | | | PY01-SEMICONDUCTORS | |
| DY01 \$ DY06 | | 9965 000 01439 | L.E.D. FY1101F-TX YELLOW CHIP | HI10010300 |
| DY07 | | 9965 000 01440 | L.E.D. SML-310DT ORANGE CHIP | HI10103210 |
| DY08 | | 9965 000 01440 | L.E.D. SML-310DT ORANGE CHIP | HI10103210 |
| DY09 | | 9965 000 01441 | L.E.D. SML-310MT GREEN CHIP | HI10104210 |
| DY18 | | 9965 000 01440 | L.E.D. SML-310DT ORANGE CHIP | HI10103210 |
| QY01 QY02 | | 9965 000 01442 4822 130 60669 | IC HD66712SA02FS LCD DRIV. CHIP TRS. 2SC4081 Q R 2SC4116 Y GR | HC10132010 HX300012A0 |
| QY05 QY06 QY07 QY08 QY09 | | 9965 000 01443 4822 130 61906 4822 130 61906 4822 130 61906 4822 130 61906 | DISPLAY UNIT LCD DIG.TRS. DTC114EU DIG.TRS. DTC114EU DIG.TRS. DTC114EU DIG.TRS. DTC114EU | HQ22801800 BA20035210 BA20035210 BA20035210 BA20035210 |
| JY01 JY03 | | | PY01MISCELLANEOUS JACK 30PIN 30FMN-BMTTR-TB JUMPER LEAD ZEBRA CONN. | YJ07016500 YU01009700 |
| SY14 \$ SY28 | | 9965 000 01445 | TACT SWITCH SKHMPW | SP01013320 |
| QY41 | | 4822 130 10161 | PY41-IR SENSOR CIRCUIT BOARD PHOTO UNIT SPS-446-4 IR SENSOR SANYO | HW10005030 |
| SY51 | 331/34 | 9965 000 01719 | PY51-PITCH DIAL CIRCUIT BOARD FOR PMD331/340 ROTARY SWITCH EC16B2410207 L=20 HOL | SR01240020 |
| | | | | |